Advertisement for the post of Scientist/Technical officer in R&D projects in VLSI carried out by

Dept. of ECE

Information to the candidates

Duties and responsibilities:

1. Carrying out R&D work in VLSI Design.

2. Guiding the UG/PG students on their project work in VLSI design & testing

3. Assisting the students during the laboratory classes on VLSI systems

4. Undergoing training on the advanced VLSI design topics under the Learning Enhancement

programme (LEP) to be conducted at the VLSI man power resource centers

5. Preparation and submission of project reports to the project funding and monitoring agencies

6. Developing the skills on advanced VLSI CAD tools and guide the students on chip design under

India chip program.

7. Any other duties that may be assigned by the project coordinator and cocoordinator

No. of posts: 1. Technical officer: One 2. Scientist: Two and 3. Lab. Engineer: One

Salary and Tenure:

The initial salary may be fixed from Rs.15, 000 to Rs. 20,000 (consolidated) depending upon the

previous experience. The post is purely temporary in nature. The appointment may initially be made for a

period of 1 year and the tenure may be extended upto a maximum of additional 1 or 2 years depending

upon the performance. The tenure will not be extended beyond 3 years.

Registration for Ph.D.: Temporary staffs, if found suitable, can register for part time Ph.D.

Qualification:

The minimum qualification is I Class M.E./M.Tech in any Circuit branch with sound working

knowledge on VLSI CAD Tools. Students who have completed the course and are awaiting results

may also apply. The application should be sent to

The HOD,

Department of ECE,

NIT, Tiruchirappalli – 620 015.

Application form and other details can be had from www.nitt.edu.

Last date for receiving application: 19.5. 2008

Superscribe the cover as "Application for the post of temporary staff"



NATIONAL INSTITUTE OF TECHNOLOGY TIRUCHIRAPPALLI- 620 015

APPLICATION FOR THE TEMPORARY POST OF TECHNICAL OFFICER/SCIENTIST/LAB. ENGINEER.

[Please tick the relevant post(s)]

Recent Passport size photograph to be affixed.

1.	1. Name in Full (Capital Letters) (as in SSLC Certificate)							
2.	2. Date of Birth			Day	Month		Y	ear
3	Address to which Communications should be sent							
	(Also furnish email, fax, telephone number, if any)							
4. Permanent Home Address								
5. Name and Address of Father / Husband								
6. De	tails of U	niversity Educati	on from SSL	C onwards				
	gree	Discipline	Unive		s/Divis on	% N	Marks	Year of Passing

7 Pr	7 Professional Experience in chronological order upto the present post					
	Organisation	Designation	From	То	Scale of Pay	
8	List of papers put	olished/ awards & distinctions	s won	1		
9	List of programm	ning languages and VLSI CA	D tools in which	ch vou have	avnarianca	
	List of programm	ing languages and veol on	D tools in write	on you have	СХРСПСПСС	
10	Liet training progr	rammes attended				
10	List training progr	animes attended				
4.4	list of subjects					
11	List of subjects taught (if any)					
	taugnt (ii any)					

13.	List of enclosures*.
14.	I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect/ false or if I do not satisfy the eligibility criteria, my candidature/ appointment is liable to be cancelled / terminated. I understand that the post is purely temporary in nature
	Place:
	Date : Signature of the Applicant

*Enclose the photo copy of the following

- (a) Details of publications/conference papers presented listing
 (b) Workshop/Training programmes attended
 (c) Awards, Patents, Prizes etc. indicating,
 (d) Any other relevant but brief information on your academic standing.
 (e) Copy of degree/diploma certificates and mark sheets