

**Advertisement for the post of Scientist/Technical officer in R&D projects in VLSI carried out by**

**Dept. of ECE**

**Information to the candidates**

**Duties and responsibilities:**

1. Carrying out R&D work in VLSI Design.
2. Guiding the UG/PG students on their project work in VLSI design & testing
3. Assisting the students during the laboratory classes on VLSI systems
4. Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI manpower resource centers
5. Preparation and submission of project reports to the project funding and monitoring agencies
6. Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
7. Any other duties that may be assigned by the project coordinator and cocordinator

**No. of posts: 1. Technical officer: One 2. Scientist: Two and 3. Lab. Engineer: One**

**Salary and Tenure:**

The initial salary may be fixed from Rs.15, 000 to Rs. 20,000 (consolidated) depending upon the previous experience. The post is purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended upto a maximum of additional 1or 2 years depending upon the performance. The tenure will not be extended beyond 3 years.

**Registration for Ph.D.:** Temporary staffs, if found suitable, can register for part time Ph.D.

**Qualification:**

The minimum qualification is I Class M.E./M.Tech in any Circuit branch with sound working knowledge on VLSI CAD Tools. Students who have completed the course and are awaiting results may also apply. The application should be sent to

**The HOD,**

**Department of ECE,**

**NIT, Tiruchirappalli – 620 015.**

Application form and other details can be had from [www.nitt.edu](http://www.nitt.edu).

Last date for receiving application: 19.5. 2008

Superscribe the cover as “Application for the post of temporary staff”



# NATIONAL INSTITUTE OF TECHNOLOGY TIRUCHIRAPPALLI- 620 015

APPLICATION FOR THE TEMPORARY POST OF TECHNICAL OFFICER/  
SCIENTIST/LAB. ENGINEER.

[Please tick the relevant post(s)]

Recent  
Passport size  
photograph to  
be affixed.

1.	Name in Full (Capital Letters) (as in SSLC Certificate)					
2.	Date of Birth	Day	Month	Year		
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3	Address to which Communications should be sent  (Also furnish email, fax, telephone number, if any)					
4.	Permanent Home Address					
5.	Name and Address of Father / Husband					
6. Details of University Education from SSLC onwards						
Degree	Discipline	University	Class/Divis ion	% Marks	Year of Passing	

7 Professional Experience in chronological order upto the present post				
Organisation	Designation	From	To	Scale of Pay

8	List of papers published/ awards & distinctions won

9	List of programming languages and VLSI CAD tools in which you have experience

10	List training programmes attended

11	List of subjects taught (if any)

