OFFICE OF THE DEAN (R & C), NIT, TIRUCHI
**ADVERTISEMENT FOR RECRUITEMENT OF**
**PROJECT STAFF**

30th November 2016

Applications are invited from Indian Nationals only for Project Positions as per the details given below, (for consultancy projects, under the Project investigator **Dr. K. Sundareswaran**, Professor, Department of Electrical and Electronics Engineering, National Institute of Technology, Tiruchirappalli-15, Tamil Nadu).

| 1. | Title of the Project | **Design And Optimization Of Feedback Controller For Boost Type Dc-Dc Converters Using Artificial Immune** |
|---|---|---|
| 2. | Funding Agency for the Project specimen | **Central Power Research Institute(CPRI), Bangalore** |
| 3. | Project Positions and Number | 1. Lab Assistant - ONE |
| 4. | Qualifications | • Diploma in EEE/ECE/CSE |
| 5. | Emoluments | Rs 12,000/- per month ( Fixed Salary and no allowances) |
| 6. | Duration | Maximum six months |
| 7. | Job Description | • To assist in laboratory work |

A.  Before appearing for the interview, the candidates shall ensure that they are eligible for the position they intend to apply.
B.  Candidates desiring to appear for the Interview should submit their applications with the following documents to the **Project Investigator – Dr. K. Sundareswaran, Professor, Department of Electrical and Electronics Engineering, NIT Trichy – 620 015** through Registered Post.
C.  Application should be furnished only in the prescribed format.
D.  Experience including research, industrial field and others.
E.  Attested copies of degree/ certificate and experience certificate
F.  Candidates shall bring along with them the original certificates at the time of interview for verification.
G.  Please note that noTA/DA is admissible for attending the interview. The decision of the selection committee is final and binding.
H.  Only shortlisted candidates will be informed by e-mail/Mobile and no separate call letter will be issued for the same.
**I.  The last date and time of submission of duly filled in application is 15th December 2016, 04.00 p.m.**
J.  The application should be submitted only in the prescribed format.

**(Dr. K. Sundareswaran)**
**Professor/ Project Investigator**

# OFFICE OF THE DEAN (R & C), NIT, TIRUCHI
## RECRUITMENT OF TEMPORARY PROJECT STAFF
## APPLICATION FOR THE POST OF LAB ASSISTANT

| For Office Use only<br><br>Serial Number allotted to the Applicant …………………………………………….<br><br><br>Signature of the Application Screening Committee Chairman | Paste here your recent passport size colour Photograph |
|---|---|

### APPLICATION FOR THE POST OF LAB ASSISTANT

| | |
|---|---|
| 1. Name of the Candidate in full | |
| 2. Nationality | |
| 3. Mother Tongue | |
| 4. Date of Birth | Date        Month        Year |
| 5. Sex | Male / Female |
| 6. Marital Status | Married / Single |
| 7. Category SC / ST / OBC / General | Please specify |
| 8. Address for communication with Mobile number and E Mail ID | |
| 9. Project Title | Design And Optimization Of Feedback Controller For Boost Type Dc-Dc Converters Using Artificial Immune System |
| 10. Advertisement Number with Date | |

Academic Career : Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPF from HSC onwards

| Name of Exam | School / University | Board/ University | Broad subjects of study | Year of Passing | % of marks / GCPA |
|---|---|---|---|---|---|
| S.S.L.C | | | | | |
| H.S.C | | | | | |
| Diploma | | | | | |
| Any other | | | | | |

Enclose self-attested Xerox copies

11. Work Experience, if any

| Organization | Designation | Duration | | Nature of Job |
|---|---|---|---|---|
| | | From | To | |
| | | | | |
| | | | | |

12.  I do not have a relative among Faculty and Staff of the NIT, Tiruchy.

13. Declaration: I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:                                                                          Full signature of the Applicant

List of Enclosures: (1)
                              (2)
                              (3)
                              (4)