Advertisement for the post of Laboratory Engineer in *Special manpower development Programme for Chip to Systems development (SMDP-C2SD) project carried out by Dept. of ECE*

**Information to the candidates**

**Duties and responsibilities:**
1. Carrying out R&D work in VLSI Design.
2. Guiding the UG/PG students on their project work in VLSI design & testing
3. Assisting the students during the laboratory classes on VLSI systems
4. Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI manpower resource centers
5. Preparation and submission of project reports to the project funding and monitoring agencies
6. Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
7. Any other duties that may be assigned by the project coordinator and cocordinator

**No. of post:** One

**Salary and Tenure:**

The initial salary may be fixed from Rs.30,000 to Rs.35,000 (consolidated) depending upon the previous experience. The post is purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended for an additional 1 year depending upon the performance.

**Registration for Ph.D.:** Temporary staffs, if found suitable, can register for part time Ph.D.

**Qualification:**

The minimum qualification is I Class M.E./M.Tech in Microelectronics/ VLSI system with sound working knowledge on VLSI CAD Tools. The application should be sent to

**The Coordinator, SMDP-C2SD project**

**Department of ECE,**

**NIT, Tiruchirappalli – 620 015.**

Application form and other details can be had from www.nitt.edu.


List of the shortlisted candidates would be displayed by 24.10.2017 in the web site www.nitt.edu.

Written test and interview of the shortlisted candidates would be held on 31.10.2017

Superscribe the cover as “Application for the post of temporary staff in SMDP-C2SD project /ECE Dept”
APPLICATION FOR THE TEMPORARY POST OF LABORATORY ENGINEER IN
SMDP-C2SD PROJECT / ECE DEPT

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8. List of papers published/ awards & distinctions won

9. List of programming languages and VLSI CAD tools in which you have experience

10. List training programmes attended

11. List of subjects taught (if any)
13. List of enclosures*.

14. I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect/false or if I do not satisfy the eligibility criteria, my candidature/appointment is liable to be cancelled/terminated. I understand that the post is purely temporary in nature.

Place :
Date :
Signature of the Applicant

*Enclose the photo copy of the following

(a) Details of publications/conference papers presented listing
(b) Workshop/Training programmes attended
(c) Awards, Patents, Prizes etc. indicating,
(d) Any other relevant but brief information on your academic standing.
(e) Copy of degree/diploma certificates and mark sheets