NATIONAL INSTITUTE OF TECHNOLOGY TIRUCHIRAPPALLI-620 015.
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date:11.03.2021

Advertisement for the post of Laboratory Engineers in *Special Manpower Development Programme for Chip to Systems Design (SMDP-C2SD)* project

Information to the candidates - Duties and responsibilities:

1. Carrying out R&D work in VLSI Design.
2. Guiding the UG/PG students on their project work in VLSI design & testing.
3. Assisting the students during the laboratory classes on VLSI systems.
4. Undergoing training on the advanced VLSI design topics under the Learning Enhancement Programme (LEP) to be conducted at the VLSI man power resource centers.
5. Preparation and submission of project reports to the project funding and monitoring agencies.
6. Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
7. Maintaining the VLSI Lab., Extended VLSI Lab. and WSD Lab.
8. Any other duties that may be assigned by the project coordinator and co-coordinator.

No. of posts: Two
Salary and Tenure:
Appointment will be made on purely contractual basis initially for a period of SIX months with a salary of Rs. 34,000/-(Rupees Thirty Four Thousand only) per month. However, the period may be extended upto November 30, 2021 depending on the satisfactory performance of the candidate.

Qualification:
Essential: B.E/ B.Tech in ECE/EEE/ICE and ME/ M.Tech in VLSI Design / Microelectronics/ any other specilization related to VLSI domain with first-class.
Desirable:
Minimum 1- year experience with the relevant field of VLSI/ASIC and must also have a strong understanding of all phases of front-end/ Back-end development in ASIC projects, including ASIC architecture, micro-architecture, RTL design, verification, timing closure & physical design.

Application form and other details can be had from www.nitt.edu.
The softcopy of the application form is to be sent to laksh@nitt.edu and the last date for receiving the application is 22/03/2021 till 5pm. The shortlisted candidates will be intimated through email to appear for written test and/or Interview.

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APPLICATION FOR THE TEMPORARY POST OF LABORATORY ENGINEERS IN SMDP-C2SD PROJECT

Recent Passport size photograph to be affixed.

1.	Name in Full (Capital Letters) (as in SSLC Certificate)	
2.	Date of Birth	Day Month Year
3	Address to which Communications should be sent (Also furnish email, fax, telephone number, if any)	
4.	Permanent Home Address	
5.	Name and Address of Father / Husband	

6. Details of University Education from SSLC onwards

Degree	Discipline	University	Class/Division	% Marks	Year of Passing

7 Professional Experience in chronological order upto the present post				
Organisation	Designation	From	To	Scale of Pay

8	List of papers published / awards & distinctions won

9	List of programming languages and VLSI CAD tools in which you have experience

10	List of training programmes attended

11	List of subjects taught (if any)

13.	List of enclosures*.
14.	I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect / false or if I do not satisfy the eligibility criteria, my candidature/ appointment is liable to be cancelled / terminated. I understand that the post is purely temporary in nature Place : Date : Signature of the Applicant

*Enclose the photo copy of the following

(a) Details of publications/conference papers presented listing
(b) Workshop/Training programmes attended
(c) Awards, Patents, Prizes etc. indicating,
(d) Any other relevant but brief information on your academic standing.
(e) Copy of all the certificates including mark sheets, experience certificates.