

**National Institute of Technology, Tiruchirappalli:  
Performa for CV of Faculty/ Staff Members**

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**Curriculum Vitae**



**Brief Profile:**

1. Name B. SHAMEEDHA BEGUM
2. Designation: ASSISTANT PROFESSOR
3. Office Address: DEPARTMENT OF COMPUTER  
SCIENCE AND ENGINEERING
4. Telephone (Direct) (Optional): 04312503200  
Telephone : Extn (Optional):  
Mobile (Optional):
5. Email (Primary): [shameedha@nitt.edu](mailto:shameedha@nitt.edu) Email (Secondary)  
:shameedhabegum@gmail.com
6. Field(s) of Specialization: Computer Architecture
7. Employment Profile

Job Title	Employer	From	To
Assistant professor	NIT, Tiruchirappalli	1/12/2008	Till date
Programmer Analyst	CTS, Coimbatore	30/7/2007	28/112008
Lecturer	CIT, Coimbatore	03/7/2004	2/8/2005

**8. Academic Qualifications (From Highest Degree to High School):**

Examination	Board / University	Year	Division/ Grade	Subjects
M.E.	Anna University, Chennai	2005 - 2007	8.86	Embedded and Real Time Systems
B.E	Avinashilingam Deemed University, Coimbatore	2001 - 2004	8.16	Computer Science
Diploma	Department of Technical	1998 - 2001	8.89	Mechanical Engineering

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	Education, Chennai			
10 <sup>th</sup> Std	Department of Government Examination, Chennai	1998	375/500	Tamil, English, Maths, Science, Social Science

### 9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	To
Committee Member	CSG	2009	2011

### 10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	To
Member of the Inspection Committee	Anna University , Chennai	2014	2015

### 11. Awards, Associateships etc.-Nil

### 12. Fellowships-Nil

### 13. Details of Academic Work

#### (i) Curriculum Development

Develoed the curriculam for Digital Systems Design and Mobile Application Development and changes made in Logical Foundations of Computer Science

#### (ii) Courses taught at Postgraduate and Undergraduate levels

- Advanced Microprocessor Systems
- Numerical Computing
- Microprocessor Systems
- Logical Foundations of Computer Science
- Digital Computer Fundamentals
- Digital Systems Design
- Mobile Applications Development
- Parallel Computer Architecture
- C Programming
- Introduction to Algorithms

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(iii) Projects guided at Postgraduate level  
- 7 Postgraduate projects guided so far.

(iv) Other contribution(s)

- Department NBA work
- Time table in-charge
- Students Mentor
- Acted as a class in-charge
- Faculty Coordinator for Vortex
- Purchased Lab equipments for B. Tech and M.Tech. CSE students.
- Department TEQIP Coordinator
- Scrutinize CSE Ph.D and M.S application
- Invigilator for CSE Ph.D Entrance Examination

14. Details of Major R&D Projects -Nil

15. Number of PhDs guided -Nil

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International / National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
27 <sup>th</sup> April to 30 <sup>th</sup> May 2015	Two Week ISTE SSTP on Introduction to algorithms	National	Participant	IIT, Kharagpur	NIT, Trichy
18 <sup>th</sup> Aug 2015	One day Digital India Workshop	National	Presenter	Deity& NIT, Trichy	NIT, Trichy
16 <sup>th</sup> -19 <sup>th</sup> Dec 2015	International Conference on Computing and Network Communications	International	Presenter	IITM-K, Trivandrum	IITM-K, Trivandrum
16 <sup>th</sup> -19 <sup>th</sup> Dec 2015	International Conference on Computing and Network	International	Presenter	IITM-K, Trivandrum	IITM-K, Trivandrum

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	Communications				
12 <sup>th</sup> -13 <sup>th</sup> August 2016	International Conference on Advances in Information Communication Technology & Computing	International	Presenter	Govt. Engineering College, Bikaner, India	Auditoriu m, Govt. Engineerin g College, Bikaner, India
29 <sup>th</sup> - 30 <sup>th</sup> January 2010	Two Day workshop on Intelligent Robotics	National	Participant	IIT, Bombay	CSED, IIT, Bombay
29 <sup>th</sup> June – 4 <sup>th</sup> July, 2009	Programme Optimization for Multi-core Architectures	National	Participant	IIT, Kanpur	CSED, IIT, Kanpur

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/  
Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
Two days Workshop on Advanced Embedded System Design on Zynq using Vivado Targeting Zed Board	National	28 <sup>th</sup> and 29 <sup>th</sup> August 2015 Co- Conven or	Co-Convenor	CSE Digital Systems Lab

18. Invited Talks delivered -Nil

19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life Member )	Organization	Membership No. with date
Professional Membership	ACM	8692366- 11-Mar-2016
Ordinary Membership	IEEE	93849938-11-Mar-2016

20. Academic Foreign Visits -Nil

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21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Pages	Year
Shameedha Begum, T Vidya, Amit D. Joshi and N Ramasubramanian	A reconfigurable Cache Design for Embedded Dynamic data cache	International Journal of Control Theory and Applications	09	8509-8517	2016
Baby Sylva, Sateesh Kumar, Shameedha Begum	Defending Against Malwares: Sand Box Detection & Prevention of Malwares in Android Devices	Journal of Computer Science Engineering and Information Technology Research (JCSEITR)	05	7-20	2015
Satyanarayana Vollala, B Shameedha Begum, Amit D Joshi, N Ramasubramanian	Bit Forwarding 3 – bits Technique for Efficient Modular Exponentiation	Journal of Cyber Security Special Issue IGI Global Publications	Accepted	Accepted	2016

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numbers	Conference Theme	Venue	Year
B Shameedha Begum, N Ramasubramanian	A comparative study of cache Performance for embedded applications	IEEE	872-876	International Conference on Computing and Network Communications	IITM-K, Trivandrum	16 <sup>th</sup> -19 <sup>th</sup> Dec 2015
Satyanarayana Vollala,	Hardware design for	IEEE	520-523	International Conference on	IITM-K,	16 <sup>th</sup> -19 <sup>th</sup>

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B Shameedha Begum, N Ramasubramanian	Multiplicative Modular Inverse based on table look up technique			Computing and Network Communications	Trivandrum	Dec 2015
Amit Djoshi, Satyanarayana Vollala, B Shameedha Begum, N Ramasubramanian	Performance analysis of Cache Coheranc Protocols for Multiucore Architectures	ACM Proceedings	22	International Conference on Advances in Information Communication Technology & Computing	Bikaner , India	12 <sup>th</sup> -13 <sup>th</sup> August 2016

(C) Books & Monographs- Nil