Curriculum Vitae

Brief Profile: P. Srinivasa Rao Nayak was received the B.Tech. degree in electrical and electronics engineering, the M.Tech. degree in energy systems and the Ph.D. degree from the Department of Electrical Engineering, National Institute of Technology, Tiruchirappalli. Currently, he is an Assistant Professor with the Department of Electrical and Electronics Engineering, National Institute of Technology, Tiruchirappalli, Tamil Nadu, India. His research interests include power electronic systems and biologically inspired optimization techniques.



1. Name: Dr. P. Srinivasa Rao Nayak

2. Designation: Assistant Professor

3. Office Address: EEE dept. NIT Trichy-15

4. Telephone (Direct) (Optional):

Telephone: 0431-2503269 Extn

(Optional):

Mobile (Optional):

5. Email (Primary):psnayak@nitt.edu Email (Secondary):

6. Field(s) of Specialization: Power Electronics Systems

7. Employment Profile

Job Title	Employer	From	То
Assistant Professor	JBREC, Hyderabad	1/09/2005	18/07/2008
Assistant Professor	NIT, Trichy	28/07/2008	Till date

8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
PhD	NITT	2014	-	Power Electronic Systems
M.Tech	JNTU Hyderabad	2006	1 st	Energy Systems
B. Tech	Nagarjuna University	2001	2 nd	Electrical and Electronics Engineering
Intermediate	Intermediate board of education	1997	3 rd	MPC
SSC	Secondary board of Education	1995	1 st	-

9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	То
Ph.D and M.Tech Admission Co- ordinator	EEE Department	2008	2009
NBA Co-ordinator	EEE Department	2010	2011
budget Co-ordinator	EEE Department	2010	2011
Staff Advisor for EEE Association	EEE Department	2010	2011
PAC Chairman for VIII Semester B.Tech EEE	EEE Department	2011	2012
Budget Co-ordinator	EEE Department	2011	2012
Staff Advisor for EEE Association	EEE Department	2012	2013
NBA Co-ordinator	EEE Department	2013	2014
Staff Advisor for EEE Association	EEE Department	2013	2014
Time table Co- ordinator	EEE Department	2014	2015
BoS Co-ordinator	EEE Department	2014	2015
Time table Co- ordinator	EEE Department	2015	2016
Staff Advisor for EEE Association	EEE Department	2015	2016

10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	То

11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization

12. Fellowships

Year of Award	Name of the Fellowship	Awarding	From	То
		Organization	(Month/Year)	(Month/Year)

13. Details of Academic Work

(i) Curriculum Development

S.No.	Institute	Duration	Title
1	NITT	2008-09	 Prepared the Lab Manual for "Electrical DC Machines Lab". Prepared the transparencies for teaching "Non Conventional Energy Sources" and distributed the copies of the same to students.
2	NITT	2009-10	 Preparation of Lab Manual for "Power Electronics Lab". Prepared the transparencies for teaching "Electrical and Electronics Measurements" course for B.Tech students and distributed the copies of the same to students.
3	NITT	2010-11	 Preparation of Lab manual for "Applied Electrical Engineering Lab". Preparation Power point presentation for "Air pollution, water pollution and global warming" for teaching the subject "Energy and Environmental Engineering".
4	NITT	2011-12	 Preparation of Lab Manual for "Power Electronics Lab". Preparation of Lab manual for "Applied Electrical Engineering Lab".
5	NITT	2012-13	1. Preparation of Lab Manual for "Power Electronics Lab".
6	NITT	2013-14	 Preparation of Lab Manual for "Power Electronics Lab". Preparation Power point presentation for teaching the subject "Industrial Electronics" and "Utilization of the Electrical Energy".

(ii) Courses taught at Postgraduate and Undergraduate levels

1				and Undergraduate levels
	S.No.	Institute	Duration	Title
	3.	NITT	2008-09	Theory: 1. Non-Conventional Energy Sources (M.Tech II Semester -EEE) 2. Electrical and Electronics Measurements (B.Tech IV Semester -EEE) Laboratory: 1. Power Electronics Lab (B.Tech VI Semester - EEE) 2. Electrical DC Machines Lab (B.Tech IV Semester - EEE) 3. Electronics Devices lab (B.Tech IV semester -EEE)
	4.	NITT	2009-10	Theory: 1. Energy and Environmental Engineering (I year B.Tech) 2. Electrical and Electronics Measurements (IV Semester – EEE) Laboratory: 1. Power Converters Lab (M.Tech I Semester – EEE)
	5.	NITT	2010-11	Theory: 1. Utilization of Electrical Energy (VIII Semester – EEE) 2. Electrical Electronics Measurements (IV Semester – EEE) Laboratory:

			1. Power Electronics Lab (VI Semester – EEE)
6	NITT	2011-12	Theory: 1. Energy and Environmental Engineering (I year B.Tech-ECE) 2. Applied Electrical Engineering (IV Semester – Mechanical) 3. HVDC Transmission (M.Tech – II Semester-EEE) Laboratory: 1. Power Electronics Lab (VI Semester – EEE) 2. Electrical Machines Lab (B.Tech IV Semester – Mechanical)
7	NITT	2012-13	Theory: 1. Industrial Electronics (III year B.Tech-EEE-VI-Semester) 2. Applied Electrical Engineering (II Semester – Mechanical) 3. Power electronic Drives (M.Tech-II Semester-EEE) Laboratory: 1. Power Electronics Lab (VI Semester – EEE)
8	NITT	2013-14	Theory: 1. Industrial Electronics (III year B.Tech-EEE-VI-Semester) 2. Applied Electrical Engineering (II Semester – Mechanical) 3. Utilization of Electrical Energy (VIII Semester – EEE) Laboratory: 1. Power Electronics Lab (VI Semester – EEE)
9	NITT	2014-15	Theory: 1. Design with PIC microcontroller (IV year B.Tech-EEE-VII-Semester) 2. Applied Electrical Engineering (II Semester – Mechanical) 4. Utilization of Electrical Energy (VIII Semester – EEE) Laboratory: Power Electronics Lab (VI Semester – EEE)
10	NITT	2015-16	Theory: 1. Design with PIC microcontroller (IV year B.Tech-EEE-VII-Semester) 2. Applied Electrical Engineering (II Semester – Mechanical) 3. Power electronic Drives (M.Tech-II Semester-EEE) Laboratory: Power Electronics Lab (VI Semester – EEE)
11	NITT	2016-17	Theory: 1. Basic Electrical and Electronics Engineering (I Semester (A sec.) – Production dept.) 2. Renewable Power Generation Technologies (M.Tech (PE) -I Semester-EEE) Laboratory: 1. Power Converters Laboratory (M.Tech (PE) -I Semester-EEE)

(iii)Projects guided at Postgraduate level

S. No.	Institute	Year	Name of the Student	Title of the Project
1.			L.Lileendra Kumar	Transient Analysis of Grid Connected PV Generation System
2.			Krishna Murthy CH	Modeling and Simulation of Load connected PV Inverter Design and Implementation of HAWT With Battery Charge Controller Circuit
3.		2009-10	M.Venkatesh naik	Variable Speed Drive Modeling and Control of Wind Turbine Design and Implementation Of Cuk Buck-Boost Converter
4.			M.Sreekanth	A New Solar Energy Conversion Scheme Implemented Using Grid –Tied Single Phase Matlab Simulation of MPPT Control For Panels Connected To DC-Dc Converter
5.	NITT		Paparao .K	 8. Modeling And Simulation of a PV Charge Control System Using SEPIC Converter 9. Design And Implementation of A PV Charge Control System Using SEPIC Converter
6.		2010-11	K. Subrahmanyam	Three Phase Bidirectional AC-DC Converter With Constant Power Factor One Cycle Controlled Bidirectional DC-AC Converter With Constant Power
7.		2011-12	Ashish ranjan rout	Design And Implementation of an Analog PID Controller Using Conventional Methods for Output Voltage Regulation In Boost Type DC-DC Converter Design And Implementation of an Analog PID Controller Using Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC Conventional Methods for Output Value Park in Land 1978 PC PC PC Conventional Methods for Output Value Park in Land 1978 PC
8.			Avinash Atla	Voltage Regulation In Buck Type DC-DC Converter 14. Study of Variable Speed Domestic Fan Behavior With A Faulty Speed Regulator 15. A New Scheme for Dynamic Braking of Capacitor –
9.		2012-13	N. Ravi	Run Induction Motors 16. Studies on Control Aspects of Washing Machine Motors 17. Power Transfer and Stability Enhancement by Simultaneous AC/DC Power Flow in EHV

				Transmission Line	
			G.Venkatesh	18. Application of Conventional Controller Design Methods of Feedback Controller for Buck –Boost type DC-DC Converter 19. Maximum Power Point Tracking of Solar PV Systems Under Partial Shading Condition Using Optimization Techniques	
			Srinivasa Reddy G	Design and Implementation of Dual Input converter inverter fed single phase capacitor run induction motor drive Design and Implementation of Dual Input converter inverter fed motor drive system incorporating solar charge controller	
10.	2013-14	Bondu Vijaya Kumar	22. Application of firefly and particle swarm optimization algorithms for solar cell parameter identification 23. Design and development of reversible speed scheme for PV powered PMDC motor		
				Duggineni Giribabu	24. Output power regulation of LED lighting scheme using Particle Swarm Optimization 25. Design and development of Dual Input LED lighting system
	NITT	NITT	Arif Shaik	26. Optimal power management scheme for PMSG based wind generation system 27. Design of feedback controller for PV fed induction motor	
11.	2014-15	2014-15	Ayoob.V.P	Application of Firefly algorithm for illumination control of LED lighting system Application of harmony search algorithm for illumination control of LED lighting system	
12.		2015-16	Katta Venkateswarlu	30. Electric vehicle route optimization under different electricity price profiles using particle SWARM optimization 31. Electric vehicle route optimization under different electricity price profiles using ANT colony	
		Bukke Vishnu Bharath	optimization 32. Power quality analysis of inverter based power		

	source for ARC welding process

(iv)Other contribution(s)

Patents:

Sl.No.	Description	Filing details
	Name of applicant: NIT, Tiruchirappalli	
	Title: A System with Multiple transmission Loss Co-efficient for Dynamic Economic	Date of Filing: 10/07/2014
1	Generator Dispatch.	Application No: 3413/CHE/2014
	Inventors: Sishaj P Simon, K Sundareswaran, Srinivasarao Nayak , C H	3413/CHE/2014
	Ram Jethmalani Name of applicant: BHEL, Trichy	
	name of apparent. BIEE, Theny	
	Title: A Batteryless Solar Photovoltaic Power Generation System to Supply Electrical Power during all Seasons to the	Date of Filing: 25/11/2014
2	Utilities Throughout the Day.	Application No:
	Inventors: Kevin Ark Kumar, Sishaj P. Simon, K. Sundareswaran, Srinivasa Rao Nayak , TT Anilkumar, C.H. Ramjeth Malani	1231/KOL/2014
	& Ratchanniya Samuel	
	Name of applicant: BHEL, Trichy	
	Title: A system to determine a day-ahead	
	loading pattern of heavy machineries in industries and proactive control of peak load	Date of Filing: 19-03-2016
3	overshoot	File No.: 201631009629
	Inventors: Muhammad Ehsan Rajith, Sishaj	
	P Simon, K. Sundareswaran, P. Srinivasa Rao Nayak , Rohit Rajan Eapen, M. Senthil	
	kumar, Kevin Ark Kumar	
	Name of applicant: NIT, Tiruchirappalli	
4	Title: A method of differential relay for power transformer protection using DSP	Date of Filing: 05/04/2016
	processor	File No: 201641012033
	<i>Inventors:</i> N. P. Padhy, Sishaj P Simon, M. Senthil kumar, K. Sundareswaran, P.	

Srinivasa Rao Nayak	
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14. Details of Major R&D Projects

Title of Project	Funding Aganay	Duration		Status
Title of Project	Funding Agency	From	То	Ongoing/ Completed
Design and Optimization of Feedback Controller for Boost type dc-dc Converters Using Artificial Immune System	Central Power Research Institute (CPRI)	2014	2017	Ongoing
Design, Development and Analysis of bio- inspired control strategies for Stand-alone solar powered LED lighting systems	MHRD Communication- NITT/SCSP-TSP	2014	2017	Ongoing
Smart Maximum Demand Control Through Modern Algorithms	Central Workshop Southern Railways (Golden Rock Workshop) Trichy	2013	2014	Completed
The Design and Development of Multi Input 10 KVA Online UPS	BHEL, Trichy	2014	2015	Completed

15. Number of PhDs guided (One Ongoing)

Name of the PhD	Title of PhD	Role(Supervisor/ Co-	Year of
Scholar	Thesis	Supervisor)	Award
		_	

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
8 th – 9 th August	Right to Information Act, 2005 and Role of Information Officer	National	Participant	NIT-Trichy	NIT-Trichy
12 th – 14 th November 2008	National Workshop on Power Electronics	National	Participant	NIT-Trichy	NIT-Trichy
1. 20 Nov 2008	Awareness programme On Intellectual property Rights	National	Participant	NIT-Trichy	NIT-Trichy
21 st – 27 th August 2008	Instructional Design and Delivery system	National	Participant	NIT-Trichy	NIT-Trichy
15th – 27th June 2009	Engineering practices On Fuzzy Logic, Neural Networks and Hybrid Intelligent Systems	National	Participant	NIT-Trichy	NIT-Trichy
29 Apr 2009	Patent Information	National	Participant	Bharathidasan University	Bharathidasan University
04 May 2009	Power Electronic Simulation – SEQUEL	National	Participant	NIT-Trichy	NIT-Trichy
12-22 Dec 2011	Solar Photovoltaic's: Fundamentals, technologies and Application	National	Participant	NIT-Trichy	NIT-Trichy
3-4 Feb 2012	Supercritical Technology for power sector	National	Participant	ESCI Hyderabad	ESCI Hyderabad
22-23 June 2012	PIC Micro controller applications in Power electronics circuits	National	Participant	NIT-Trichy	NIT-Trichy
05 Jan 2013	MSP 430 Microcontroller Based System Design"	National	Participant	NIT-Trichy	NIT-Trichy
16 Dec 2012	Resonant and soft switching power conversion and three more topics	National	Participant	PEDES-2012 Bangalore	PEDES Bangalore
28-29 April 2015	Conclave on academic reforms(CAR-2015)	National	Participant	NIT-Trichy	NIT-Trichy

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convene)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
One day workshop on "Application of Biologically Inspired Algorithms for Power System and Power Electronics Engineering" Under the Self- Financed Category	National	28 th July 2012	Coordinator	NIT Trichy
One Day Workshop on Fuzzy Systems and Applications Under the Self-Financed Category	National	13th July 2013	Coordinator	NIT Trichy
Three Day Conclave On Academic Enhancements in Electrical Engineering (Power System and Power Electronic Streams) Under TEQIP-II	National	8th -10th November 2013	Coordinator	NIT Trichy
One Day Workshop On Particle Swarm Optimization, Applications and Implementation on a Microcontroller Under self finance category	National	8th March 2014	Coordinator	NIT Trichy
Two Day Workshop On Implementation of Firefly Algorithm in SCILAB and μ-Controller Under self finance category	National	10-11th October 2014	Coordinator	NIT Trichy
One week Workshop On Recent Developments in Electrical Power Engineering Under TEQIP II	National	29 th Aug – 3 rd Sept 2016	Coordinator	NIT Trichy

18. Invited Talks delivered

Topic	Date	Inviting Organization

19. Membership of Learned Societies

Type of Membership (Ordinary	Organization			Membership No. with	
Member/ Honorary Member /					date
Life Member)					
Life Member	Indian	Society	for	Technical	LM80445/2011
	Education	on (ISTE)			

Life Member	Solar Energy Society of India (SESI)	LM/1435/2010
Life Member	System Society of India(SSI)	LM31941/2011

20. Academic Foreign Visits

Country	Duration of Visit	Programme

21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
K.Sundareswaran, P.S.Nayak	Ant colony based feedback controller design for soft- starter fed induction motor drive	Applied Soft Computing, Elsevier	Vol. 12, No. 5, pp	1566- 1573	May 2012	2
K.Sundareswaran, Devi V, S. Sankar, PSR Nayak, A. Chandra sekar	Feedback controller Design for a Buck Converter Through Evolutionary Algorithms	Australian Journal of Electrical & Electronics Engineering	Vol.10, No.4	459- 466.	2014	
K.Sundareswaran, PSR Nayak	Particle Swarm Optimization Based Feedback Controller Design for Induction Motor Soft- Starting	Australian Journal of Electrical & Electronics Engineering	Vol 11, No.1	55-63.	March 2014	
Kinattingal Sundareswaran, Vadakke Devi, Selvakumar Sankar, Panugothu Srininivasa Rao Nayak, Sankar Peddapati	Feedback controller Design for a Boost Converter Through Evolutionary Algorithms	IET Power Electron	Vol. 2, No. 1	1-11	Oct 2013	

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Sundareswaran, K. and Nayak, P.S.R.	Design of Feed Back Controller for Soft-starting Induction Motor Drive System Using Genetic Algorithm	Int. J. Industrial Electronics and Drives, Inder Science Publishe <u>r</u>	Vol. 1, No. 2,	111– 120	March 2014	
K.Sundareswaran, P.S.R.Nayak and A.Chandra Sekhar,	Development of an Improved Particle Swarm Optimization (PSO) and its Application to Induction Motor Soft-Starting	International Review of Automatic Control, Praise worthy prize	Vol. 7, No. 2	156-165	March 2014	
Kinattingal Sundareswaran and Panugothu Srinivasa Rao Nayak,	Optimization of Induction Motor Soft-Starting through Artificial Immune System	Electrical Power Components and Systems, Taylor & Francis	accepted for publication		2016	
K.Sundareswaran, V. Vigneshkumar, P. Sankar, S.P. Simon, P.S.R. Nayak , and S. Palani	Development of an improved P&O Algorithm Assisted Through a Colony of Foraging Ants for MPPT in PV System	IEEE Transactions on Industrial Informatics	Vol. 12, No.1	187-200	February 2016	
Anilkumar T.T., Sishaj P Simon, P. Srinivasa Rao Nayak, K. Sundareswaran and Narayana Prasad Padhy,	Pico-Hydel Hybrid Power Generation System with an Open Well Energy Storage	IET Gen. Trans. Dist	Accepted for Publication		2016	
M. Senthil kumar, Sishaj P Simon, P. Srinivasa Rao Nayak, K. Sundareswaran and Narayana Prasad Padhy	An Empirical Fourier Transform Based Power Transformer Differential Protection	IEEE Transactions on Power delivery	Accepted for Publication		2016	
K.Sundareswaran, P. Sankar, P.S.R. Nayak , S.P. Simon and S. Palani,	Enhanced Energy Output From a PV system under partial shaded conditions through artificial Bee Colony	IEEE Transactions on Sustainable Energy	Vol. 6, No. 1	198-209	January 2015	8

K.Sundareswaran, P.S.R. Nayak, P. Sankar and V. Vigneshkumar	Inverter Harmonic Elimination Through Flower Pollination Enhanced Genetic Algorithm	International Journal of Advanced Trends in Computer Science and	Vol. 3 , No.1	342 – 348	February 2014	
Ram JC Hemparuva, S.P.Simon, S.Kinattingal, SRN Panugothu	Gravitational Search Algorithm- Based Dynamic Economic Dispatch by estimating Transmission System Losses using A-Loss Coefficients	Engineering Turkish Journal of Electrical Engineering And Computer Science, 2016	Accepted for Publication		2016	
S. Kumar Murugan, S.Simon, P. Nayak , K.Sundareswaran, N.P. Padhy	Power Transformer Protection using Chirplet Transform	IET Generation, Transmission and Distribution,	Accepted for Publication		2016	
Ram JC Hemparuva, S.P.Simon, K.Sundareswaran, P.S.R. Nayak	Auxilliary Hybrid PSO BPNN based transmission losses estimation in Generation Scheduling	IEEE Industrial Informatics, 2016	Accepted for Publication		2016	8

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numb ers	Conferen ce Theme	Venue	Year
K.Sundareswaran, P.Srinivasarao Nayak, Ch Durga Venkatesh	Induction Motor Starting Dynamic optimization Using Random Search method	Second International Conference on Advances in Control and Optimization of Dynamical Systems (ACODS)	1-4		IISC Bangalore	2012
K.Sundareswaran, P.Srinivasarao Nayak, Ch Durga Venkatesh and	Optimal Placement of FACTS Devices using Probabilistic Particle Swarm	IEEE PES International Conference on Innovative Smart Grid Technologies	53-58		Kollam, Kerala,	2011

Hariharan B	Optimization	(ISGT)			
		IEEE			
K. Sudareswaran, Hariprasad B, P. Sankar, P.Srinivasa Rao Nayak and S. Sankar	A Voltage Constrained Time Sharing Switching Scheme for Dual Input Buck Converter	International conference on Power Electronics, Drives and Energy Systems (PEDES)	1-5	CPRI, Bangalore	2012
K. Sundareswaran, P.Sankar, and P.Srinivasa Rao Nayak	Analysis on the Failure of Dynamic Braking of Capacitor-Run Induction Motor Supplied from Half-Controlled Converter	IEEE International conference on Power Electronics, Drives and Energy Systems (PEDES)	1-5,	CPRI, Bangalore	2012
K. Sundareswaran, S.Sankar, P.Srinivasa Rao Nayak	Feedback controller Design for a Buck-boost Converter through Evolutionary Algorithms	IEEE International conference on Power Electronics, Drives and Energy Systems (PEDES),Dec. 2012.	1-7	CPRI, Bangalore ,	2012
K. Sundareswaran, Kuruvinashetti Kiran, Varsha Padhee, P Sankar, P. Srinivasa Rao Nayak, Abhilash Mahadevan	Buck-Boost Converter Controller Design Using Bacterial Foraging	IEEE Multi- conference on Systems and Control (IEEEMSC), Aug. 2013.	995- 999	Hyderaba d	2013
K. Sundareswaran, Kuruvinashetti Kiran, Hariprasad.B, P Sankar, P. Srinivasa Rao Nayak	Output Voltage Controller of Dual Input Buck-Boost Converter	IEEE PES International Conference on Innovative Smart Grid Technologies (ISGT)		Bangalore ,	2013
K. Sundareswaran, Kuruvinashetti Kiran, P Sankar, V. Vignesh Kumar, P. Srinivasa Rao Nayak	Output Voltage Control and Power Management of a Dual Input Buck- Boost Converter	Third International conference on Advances in Controls and Optimization in		IIT Kanpur.	2014

	Employing P and O Algorithm	Dynamical Systems.(IFAC), March 2014.		
K. Sundareswaran, Kuruvinashetti Kiran, P Sankar, V. Vignesh Kumar, P. Srinivasa Rao Nayak	Optimization of Dual Input Buck Converter Control Through Genetic Algorithm	Third International conference on Advances in Controls and Optimization in Dynamical Systems.(IFAC)	IIT Kanpu	2014
K. Sundareswaran, Kuruvinashetti Kiran and P. Srinivasa Rao Nayak	Application of Particle Swarm Optimization for Output Voltage Regulation of Dual Input Buck-Boost Converter	Second International conference on ICGCCEE-14,	Coimbator e, Tamil Nadu.	2014
Dharavath Kishan, P.S.Nayak	Wireless Power Transfer Technologies For Electric Vehicle Battery Charging- A State Of The Art	SCOPES- 2016	Centurion University , Odisha.	2016

(C) Books & Monographs

Author(s)	Title of Book/Monograph	Name of	Year of	ISSN/ISBN
		Publishers	Publication	Number