

National Institute of Technology, Tiruchirappalli: Performa for CV of Faculty/ Staff Members



I am S.Mageshwari completed M.E in Electronics from the Madras Institute of Technology in 2005 and its currently working as Assistant Professor in the Department of EEE in the National Institute of Technology Tiruchirappalli for the past 10 years.

1. Name: S.MAGESHWARI
2. Designation: ASSISTANT PROFESSOR
3. Office Address: EEE DEPARTMENT, NIT.
4. Telephone (Direct) (Optional):
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:magesh2909@gmail.com
6. Field(s) of Specialization: Analog and Digital Electronics
Computer Networks
Power Electronics and Renewable energy system.

7. Employment Profile

Job Title	Employer	From	To
Sri Sai Ram Engineering College, Chennai.	Lecturer	06.07.2005	28.03.2006
National Institute of Technology, Trichy.	Assistant Professor	29.03.2006	Till date

8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
Ph.D Degree	NITT	2008	Pursuing	PV fed Grid Connected Residential

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				Appliances.
M.E	MIT, Anna University	2005	8.5	Electronics Engineering
B.E	GCT/Bharathiyar	2002	77.84%	EEE
HSC	STATE BOARD	2000	85%	MATHS-BIOLOGY
SSLC	STATE BOARD	1998	85%	MATHS-SCIENCE

9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/ Centre/Institution	From	To
M.Tech/Ph.D Admission Co-ordinator & BOS Syllabus &Curriculum In charge	EEE	2006	2008
Electronics Lab Purchase Committee Memeber	EEE	2007	2008
Department Library in charge	EEE	2007	2008
Department Co-ordinator for NITTFEST	EEE	2008	2009
Physical Stock Verification Committee Member	EEE	2008	2015
NBA Co-ordination Committee member	EEE	2008	2017
PAC Chairman for I year I Sem M.Tech(PE)	EEE	2009	2010
Members of Convocation Reception Committee	EEE	2009	2010
Horizontal Review Audit Preparation &BOS Time table In charge	EEE	2010	2011
PAC Chairman for I year I Sem M.Tech(PS)	EEE	2011	2012
Students Counselling Coordinator	EEE	2012	2013
Website in charge	EEE	2012	2013
House Keeping coordinator	EEE	2013	2014
M.TechProgrammeCoordinator(Power Electronics)	EEE	2014	2015
Electronics Lab In charge	EEE	2013	2016
Electronics lab and Power Electronics Lab In charge	EEE	2016	2017
Class Committee Chair person 4 th Semester B.Tech	EEE	2016	2017

10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	To
NIL	NIL	NIL	NIL

11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization
NIL	NIL	NIL

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12. Fellowships

Year of Award	Name of the Fellowship	Awarding Organization	From (Month/Year)	To (Month/Year)
NIL	NIL	NIL	NIL	NIL

13. Details of Academic Work

(i) Curriculum Development: Modified in Circuits and Devices Lab and Electronic Circuit Experiments

(ii) Courses taught at Postgraduate and Undergraduate levels:

M.Tech Programme (Power Electronics)

- Computer Networks
- Fuzzy System
- Industrial Control Electronics

B.Tech Programme (EEE)

- Electron Devices
- Digital Electronics
- Operating System
- Computer Architecture

B.Tech Programme (first Year)

- Basic Programming in C
- Basic Electrical and Electronics Engineering

B.Tech Programme (Production Engineering)

- Applied Electronics

B.Tech Programme (Chemical Engineering)

- Digital Electronics

(iii) Projects guided at Postgraduate level:19

S. N O	Institute	year	Name of the student	Title of the Project
1	NITT	2007-08	Venu Gopal P.V.B	A variable structure control for single ended Primary inductance Converter
2.	NITT	2007-08	Mary Beula. A	Analog Implementation of single phase half – Bridge PWM Rectifier and Simulation of its Application to Transformerless UPS
3.	NITT	2008-09	K.Suman Babu	Simulation of Closed Loop Speed Control of Separately Excited DC motor using Buck Converter
4.	NITT	2009-10	O.Prudhui Raj	1. Control of High Frequency AC link Electronic Transformer. 2.A KY Boost Converter
5.	NITT	2009-10	E.Ramdas	1.Small Signal Stability Analysis of SSSC compensated Power System 2.Simulation and analysis of inverter switch open faults
6.	NITT	2009-10	A.Nagaraju	1.Modelling and Control of PV fed Zeta Converter

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				2.Hardware Implementation of PV fed Zeta Converter
7.	NITT	2011-12	Y.Goutham Kuma	Design of High frequency and High performance voltage regulator module
8.	NITT	2011-12	Savara Vasudeva	Accurate Maximum Power Point Tracker of PV System
9.	NITT	2011-12	Ramesh Manda	DC-DC converter using Cascade Technique
10.	NITT	2012-13	J.Senthilnathan	Closed Loop Automatic PWM based Reversible speed control of DC motor using Microcontroller
11.	NITT	2012-13	C.B.Subramonian	Speed Control of Induction Motor using Artificial Intelligent Techniques
12.	NITT	2012-13	Savara Vasudeva	Distributed FACTS –A new concept for Realizing Grid Power Flow
13.	NITT	2013-14	O.Rakesh Pal	1 .Integration of PV and Battery Hybrid System to the Grid. 2.Control of DSTATCOM by using IcosØ Algorithm
14.	NITT	2013-14	Hanumatha Rao	1. A Buck-Boost Based DC/AC Converter for Residential PV Application. 2.Hardware Implementation of a Buck-Boost Based DC/AC Converter for Residential PV applications
15.	NITT	2014-15	G.Ganesh	1 .A Buck-Boost Based DC/AC converter for Residential PV Applications Using Fuzzy Logic Controller. 2.Hardware Implementation of Buck-Boost Based DC/AC converter for residential PV applications
16.	NITT	2014-15	A.Subhash Kumar Reddy	Analysis and Implementation of Bidirectional DC-DC Converter using Coupled Inductor in Stand-alone PV application
17.	NITT	2014-15	Mithuan	1. Design of an Embedded adaptive Fuzzy logic Controller for a single Phase inverter. 2. FPGA Implementation Single phase Grid Connected Inverter.
18.	NITT	2015-16	R.Venkataeswara Reddy	1. Current Fed Soft Switching PUSH-PULL based DC-DC converter fed with PV for Grid Connected System. 2. Hybrid switched Mode Power Supply System Using the Solar Cell.
19.	NITT	2015-16	M.Nagaraju	1. An Interleaved Boost converter with Zero-Voltage Transition. 2. Hardware Implementation of an interleaved Boost Converter with Zero Voltage Transition.

(iv)Other contribution(s)

14. Details of Major R&D Projects

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Title of Project	Funding Agency	Duration		Status
		From	To	Ongoing/ Completed
Power Optimization Controller in Switched Mode Power Supplies Connected with Renewable energy Sources	MHRD	2014	2017	ongoing

15. Number of PhDs guided

Name of the PhD Scholar	Title of PhD Thesis	Role(Supervisor/ Co-Supervisor)	Year of Award
Nil	Nil	Nil	Nil

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
15 th -17 th april 2006	Work Shop on “Cyber Laws”	National	Participant	NIT Warangal	Andra Pradesh
4 th -6 th Jan 2007	Embedded Systems on chip	5 th International Conference TIMA	Participant	NIT	Trichy
12 th -16 th june 2006	Short Term Course on “Microcontroller and Application”	National	Participant	NITTTR	Chennai
12 th -16 th March 2007	Train the Trainer Programme	National	Participant	NITT ,Trichy	Chennai
4 th -15 th June 2007	Two Week Course on Analog VLSI Design	National	Participant	TCE	Madurai
17 th -18 th August 2007	Workshop on” Overview of VLSI – CAD Tools”	National	Participant	NIT	Trichy
20 th -27 th August	Workshop on “Wind Power Integration”	National	Participant	NIT	Trichy

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2007					
12 th -13 th Oct- 2007	All India Workshop on Recent Trends in Energy Management	National	Participant	The Institution of Engineers(India)	Trichy
27 th Dec 2007	3 rd National Conference on ICECON2007 “Preconference Tutorial on SCILAB”	National	Participant	NIT-ICE department	Trichy
2 nd -3 rd Jan 2008	IEEE workshop on “Energy,Environment and Importance of Power Electronics”	National	Participant	IIT	Delhi
29 th feb 2008	Workshop on “Multivariable Control Systems”	National	Participant	NIT(Chemical Dept)	Trichy
28 th -1 st feb 2008	Sort Term course on “Electrica; Machines and Power Electronics in Renewable energy Systems”	National	Participant	NIT(EEE)	Trichy
16 th -28 th june 2008	“Power Electronics and Renewable Energy Electric Conversion Systems”	National	Participant	MHRD-AICTE NITT(EEE)	Trichy
7 th Aug 2008	“Analog VLSI Filters and Mixed signal Design”	National	Participant	ECIT	Banglore
7 th -12 th Aug 2008	Induction Training Programme on “ Instructional Design and Delivery”	National	Participant	NITTTR	Chennai
24 th -28 th March 2008	Five day Residential Sensitization workshop under Capacity building of Women Managers in Higher education	National	Participant	Bharathidasan University(CWS)	Trichy
19 th Feb 2009	Computer Networking – Hardware&Software	National	Participant	M/s SANAT Tech,Chennai	Trichy
4 th may 2009	“Power Electronics Simulation- SEQUEL”	National	Participant	NaMPET	Trichy
18 th feb 2010	Welding Power Source Evaluation	National	Participant	IWS &WRI	Trichy

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	&Process Control				
20 th -21 st Dec 2010	High Voltage DC Transmission Systems	National	Participant	CPRI	Banglore
13 th -14 th July2012	“FPGA Based System Design”	National	Participant	NIT(EEE)	Trichy
5 th -6 th Oct2012	“Class Room Management &Communication”	Local	Participant	NIT	Tanjore
4 th -5 th Jan 2013	“Wind Energy Electric Conversion Systems”	National	Participant	NIT(EEE)	Trichy
11 th -12 th Oct 2013	“Hybrid Power generation Systems	National	Participant	CPRI	Banglore
2 nd -12 th Dec 2014	Two week ISTE workshop on “Control Systems”	National	Participant	National Mission on Education through ICT(MHRD)	NIT, Trichy
28 th -29 th April 2015	“Conclave on Academic Reforms”	National	Participant	TEQIP-II	NIT, Trichy
4 th -5 th March 2016	Short Term Course on“Role of Power Electronics in Power Engineering”	National	Participant	TEQIP- II(NIT)EEE	Trichy

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/
Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
Workshop on Electronic Circuit Design Techniques	National	28-09- 2007 &29- 09-2007	Coordinator	NITT
Finishing School Programme	National	May & June 2007 &2008	Coordinator	NITT
Workshop on MSP 430 Microcontroller Based System Design	Local	Feb 23&24 2013	Coordinator	NITT
Workshop on Real time	National	Sep 26 th	Coordinator	NITT

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controller Design Using C2000 Microcontroller		& 28 th 2014		

18. Invited Talks delivered

Topic	Date	Inviting Organization
Nil	Nil	Nil

19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life Member)	Organization	Membership No. with date
Annual Member	IEEE Membership	92598528 & 11.02.2016

20. Academic Foreign Visits

Country	Duration of Visit	Programme
Nil	Nil	Nil

21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
S.Mageshwari	Simplified Topology for single phase to three phase conversion for Induction Motor Drive using a single phase half-bridge PWM Boost Rectifier and a Three-leg inverter	International Journal of Power systems and Power electronics	4		2011	

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numbers	Conference Theme	Venue	Year

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S.Mageshwari	Distributed Network Monitors and Network Traffic Generators	VESCOM 2005		VESCOM 2005	KEC Salem	2005
S.Mageshwari	Modified fast linear Load flow method inducing Generator Q limits	CIPS 2006		CIPS 2006	SRM, Chennai	2006
S.Mageshwari	Simplified Topology for single phase to three phase conversion for Induction Motor Drive using a single phase half –bridge PWM Boost Rectifier and a Three-leg inverter	NCEEES		NCEEES	Anna University chennai	2008
S.Mageshwari	A High efficiency Buck- Boost DC/DC Converter for Residential PV Application	IC31CT- 2015		IC31CT- 2015	Bangalore	2015
S.Mageshwari	Multimodel Based Control of pH Neutalization Process	66 th Canadian Chemical engg conference		66 th Canadian Chemical engg conference	Canada	2016

(C) Books & Monographs

Author(s)	Title of Book/Monograph	Name of Publishers	Year of Publication	ISSN/ISBN Number
Nil	Nil	Nil	Nil	Nil