

National Institute of Technology, Tiruchirappalli: Performa for CV of Faculty/ Staff Members

Curriculum Vitae

Kinattungal Sundareswaran was born in Pallassana, Kerala, India, in 1966. He received the B.Tech. (Hons.) degree in electrical and electronics engineering and the M.Tech. (Hons.) degree in power electronics from the University of Calicut, Calicut, Kerala, India, in 1988 and 1991, respectively, and the Ph.D. degree in electrical engineering from Bharathidasan University, Tiruchirappalli, Tamil Nadu, India, in 2001.



From 2005 to 2006, he was a Professor with the Department of Electrical Engineering, National Institute of Technology, Calicut, Kerala, India. He is currently a Professor with the Department of Electrical and Electronics Engineering, National Institute of Technology, Tiruchirappalli, Tamil Nadu, India. His research interests include power electronics, renewable energy systems, and biologically inspired optimization techniques.

1. Name: **Dr. K. Sundareswaran**
2. Designation: Professor
3. Office Address: Department of EEE, National Institute of Technology, Tiruchirappalli, TamilNadu 620 015
4. Telephone (Direct) (Optional): 0431 - 2500701
Telephone : Extn (Optional):
Mobile (Optional): 09443766518
5. Email (Primary): kse@nitt.edu Email (Secondary) :
6. Field(s) of Specialization: Power Electronics,
Applications of Biologically inspired optimization algorithms in Electrical power engineering

7. Employment Profile

Job Title	Employer	From	To
Professor	National Institute of Technology, Tiruchirappalli,	23 April, 2007	Till date
Asst. Professor		31 March 2006	22 April 2007

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

	TamilNadu 620 015		
Professor	National Institute of Technology, Calicut, Kerala	28 Sept 2005	30 March 2006
Asst. Professor	National Institute of Technology, Tiruchirappalli, TamilNadu 620 015	24 June 1998	26 Sept 2005
Lecturer		08 Nov 1990	23 June 1998

8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
Ph.D.	Bharathidasan University	2001		EEE
M.Tech	University of Calicut	1991	First Class (Hons.)	Power Electronics
B.Tech	University of Calicut	1988	First Class (Hons.)	Electrical Engineering
Pre-degree	University of Calicut	1983	First Class	Maths, Phsyics, Chemistry
SSLC	Department of Education, Kerala State.	1981	First Class	

9. Academic/Administrative Responsibilities within the University:

Position	Faculty/Department/Centre/Institution	From	To
Head of the Department	Electrical and Electronics Engineering, National Institute of Technology, Tiruchirappalli	Jan 2015	24.01.2018

10. Academic/Administrative Responsibilities outside the University: Nil

Position	Institution	From	To

National Institute of Technology, Tiruchirappalli: Performa for CV of Faculty/ Staff Members

11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization
2007	Best Teacher Award	National Institute of Technology, Tiruchirappalli
2018	Certificate of Appreciation in recognition for significant contribution to the growth of the Institution through Citations(Web of Science)	National Institute of Technology, Tiruchirappalli

12. Fellowships: Nil

Year of Award	Name of the Fellowship	Awarding Organization	From (Month/Year)	To (Month/Year)

13. Details of Academic Work

(i) Curriculum Development: UG and PG Courses

(ii) Courses taught at Postgraduate and Undergraduate levels

Power electronics and drives, Fuzzy systems, Electron Devices, Solid State Drives, Power electronics.

(iii) Projects guided at Postgraduate level: 40

(iv) Other contribution(s): Nil

14. Details of Major R&D Projects

Title of Project	Funding Agency	Duration		Status
		From	To	Ongoing/ Completed
Applications of Genetic Algorithm for high performance Power Converters (Coordinator)	MHRD	2003	2005	Completed
In House Development of Solar Led Street Lights (as Team Member)	BHEL, Trichy	08.04.2013	2014	Completed

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

In-House Development of Multi Input 10kVA Online UPS (as Team Member)	BHEL, Trichy	15.09.2014	2015	Completed
Design, Development and Analysis of Bio-Inspired Control Strategies for Stand Alone Solar Powered Led Lighting Systems (Co-Coordinator)	SCSP/TSP Project (NIT Trichy)	11.02.2014	2017	Completed
Design and Optimization of Feedback Controller for Boost Type DC-DC Converters Using Artificial Immune System (Coordinator)	CPRI, Bangalore	28.05.2014	2017	Completed

15. Number of PhDs guided

Name of the PhD Scholar	Title of PhD Thesis	Role(Supervisor/ Co-Supervisor)	Year of Award
N. Rajasekar	Development and analysis of energy efficient speed control techniques for capacitor induction motor drives.	Supervisor	2007
V.T. Sreedevi	Performance enhancement of certain power electronic systems using optimization algorithms based on a colony of honeybees.	Supervisor	2009
Bos Mathew Jos	Development and analysis of novel starting methods for AC voltage controller fed induction motor.	Supervisor	2010
V. Devi	Application of biologically inspired optimization algorithms towards feedback controller design for buck and boost type dc-dc converters.	Supervisor	2012
Srinivasa Rao Nayak P.	Design, implementation and analysis of a few recently developed optimization algorithms for induction motor soft	Supervisor	2014

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

	starting.		
Sankar Peddapati	Design, implementation and analysis of a few optimization algorithms for maximum power point tracking in pv systems under partial shaded conditions	Supervisor	2015
Kevin A. K.	Design, development and testing of solar photovoltaic power conversion systems for industrial applications	Supervisor	2016
Vignesh Kumar V.	Photo Voltaic Power generation systems	Supervisor	2018
Thomas P. Rajan	Industrial Applications of power electronics	Supervisor	In Progress
Manujith P. S.	Electrical Power Engineering	Supervisor	In Progress
Satheesh Krishnan G	Electrical Power Engineering	Supervisor	In Progress
T Manikandan	Electrical Power Engineering	Supervisor	In Progress
Gireesh V Puthusserry	Electrical Power Engineering	Supervisor	In Progress

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
2 nd March 2013	Workshop on Fuzzy control in electrical science	National	Speaker	Electrical and Electronic Technocrats Association (EETA) of EEE Department	J.J College of Engg. & Technology, Tiruchirappalli
9 th March 2013	One day workshop on, "Intelligent Techniques based on Fuzzy and GA"	National	Speaker	Association of EEE	P.A. College of Engg. & Technology, Pollachi

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

2 nd & 3 rd Feb 2011	Two day workshop on, “Soft Computing Techniques in Power Electronics Systems (SCTPES)”	National	Speaker	Department of EEE	Anna University of Technology, Tiruchirappalli
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17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
One day workshop on, “Application of Biologically Inspired Algorithms for Power System and Power Electronics Engineering” Under the Self-Financed Category	National	28 th July 2012	Convenor	NITT
One Day Workshop on, “Fuzzy Systems and Applications” Under the Self-Financed Category	National	13th July 2013	Convenor	NITT
Three Day Conclave On, “Academic Enhancements in Electrical Engineering” (Power System and Power Electronic Streams) Under TEQIP-II	National	8th -10th November 2013	Convenor	NITT
One Day Workshop On, “ Particle Swarm Optimization, Applications and Implementation on a	National	8th March 2014	Convenor	NITT

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

Microcontroller” Under self finance category				
Two Day Workshop On, “Implementation of Firefly Algorithm in SCILAB and μ -Controller” Under self finance category	National	10-11th October 2014	Convenor	NITT
One week Workshop On, “Recent Developments in Electrical Power Engineering” Under TEQIP II	National	29 th Aug – 3 rd Sept 2016	Convenor	NITT

18. Invited Talks delivered:

Topic	Date	Inviting Organization
Solid State Drives	17 th April 2013	P.A. College of Engg. & Technology, Pollachi

19. Membership of Learned Societies:

Type of Membership (Ordinary Member/ Honorary Member / Life Member)	Organization	Membership No. with date
Fellow	Institution of Engineers (India)	Feb 2014

20. Academic Foreign Visits:

Country	Duration of Visit	Programme
Singapore	16 th June – 13 th July 2007	Training under TEQIP

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
C. H. Ram Jethmalani, Sishaj P Simon, Kinattingal Sundareswaran, P. Srinivasa Rao Nayak, Narayana Prasad Padhy	Auxiliary Hybrid PSO-BPNN- Based Transmission System Loss Estimation in Generation Scheduling	IEEE/ Industrial Infomatics/	Vol. No.13, Issue. 4	1692 - 1703	2017	4.708
Senthil Kumar Murugan, Sishaj P Simon, Kinattingal Sundareswaran, P. Srinivasa Rao Nayak, Narayana Prasad Padhy	An Empirical Fourier Transform-Based Power Transformer Differential Protection	IEEE/Power Delivery	Vol. No. 32, Issue. 1	209 - 218	2017	3.218
K. Sundareswaran, V. Vigneshkumar, P. Sankar, Sishaj P Simon, P. Srinivasa rao nayak and S. Palani	Development of an Improved P&O Algorithm Assisted Through a Colony of Foraging Ants for MPPT in PV System	<i>IEEE Transactions on Industrial Informatics</i>	Volume No. 12, issue. 1	pp. 187- 200	2016	4.708
K. Sundareswaran, V. Vigneshkumar and S. Palani	Development of a hybrid genetic algorithm/perturb and observe algorithm for maximum power point tracking in photovoltaic systems under non-uniform insolation	<i>IET Renewable power generation</i>	Volume No. 9	pp. 1-9	2015	1.562

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

K. Sundareswaran, P.Sankar, P. Srinivasa Rao Nayak, Sishaj P Simon and S. Palani,	Enhanced Energy Output from a PV System Under Partial Shaded Conditions Through Artificial Bee Colony	<i>IEEE Transactions On Sustainable Energy,</i>	Volume: 6, Issue: 1	pp. 198- 209,	2015	6.30
Kevin Ark Kumar, K. Sundareswaran and P.R. Venkateswaran	Performance study on a grid connected 20 kW solar photovoltaic installation in an industry in Tiruchirappalli (India)	Energy for Sustainable Devolpment,		pp. 294- 304,	2014.	2.379
K. Sundareswaran, V. Vignesh kumar and S. Palani	Application of a combined particle swarm optimization and perturb and observe method for MPPT in PV systems under partial shading conditions	Renewable Energy	vol. 75	pp. 308- 317	2015	3.404
K. Sundareswaran, P. Sankar, S. Palani,	MPPT of PV Systems Under Partial Shaded Conditions Through a Colony of Flashing Fireflies,	<i>IEEE Transactions On Energy Conversion</i>	Vol. 29	pp. 463- 472	2014.	4.24
K. Sundareswaran, P. Sankar, S. Palani,	Application of random search method for maximum power point tracking in partially shaded photovoltaic systems	<i>IET Renewable Power Generation</i>	Vol. 8	pp 670 – 678	2014.	3.16
K.Sundareswaran, P.S.R.Nayak and	Development of an Improved	International Review of	Vol. 7, No. 2	pp. 156- 165	March 2014	0.71

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

A.Chandra Sekhar, Praise worthy prize, ,	Particle Swarm Optimization (PSO) and its Application to Induction Motor Soft-Starting	Automatic Control				
Sundareswaran, K. and Nayak, P.S.R.	Design of Feed Back Controller for Soft-starting Induction Motor Drive System Using Genetic Algorithm	Int. J. Industrial Electronics and Drives (IJIED)	Inder Science Publisher, Vol. 1, No. 2	pp.111– 120	March 2014	
K.Sundarewaran, P. Srinivasarao Nayak	Particle Swarm Optimization Based Feedback Controller Design for Induction Motor Soft-starting	Australian Journal of Electrical & Electronics Engineering	Vol. 11, no. 1	pp. 55-63	2014	0.16
K.Sundareswaran and V.T.Sreedevi,	Inverter Harmonic Elimination using Honey Bee Intelligence	Australian Journal of Electrical and Electronics Engineering	Vol. 6, No. 2	pp. 153- 164	2009	0.29
K.Sundareswaran and V.T.Sreedevi,	Design and Development of Feed-back Controller for a Boost Converter using a Colony of Foraging Bees	Electric Power Components and Systems	Vol. 37, No. 5	pp. 465- 477	May 2009	0.349
K.Sundareswaran and V.T.Sreedevi,	Boost converter controller design using Queen Bee assisted GA	IEEE Transactions on Industrial Electronics	Vol. 56, No. 3	pp. 778- 783	March 2009	6.80
K.Sundareswaran, Krishna Jayant, T.N.Shanvas	Inverter harmonic Elimination through a colony of continuously exploring ants	IEEE Transactions on Industrial Electronics,	Vol.54, No. 5,	pp. 2558- 2565,	Oct. 2007	3.73
K.Sundareswaran, N.Rajasekar and V.T.Sreedevi	Performance Comparison of Capacitor Run Induction Motors	IEEE Transactions on Industrial Electronics	Vol.53, No. 3	pp.990- 993	June 2006	2.07

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

	Supplied from AC Voltage Regulator and SPWM AC Chopper					
K.Sundareswaran and Bos Mathew Jos	Development and Analysis of a Novel Soft-Starter/Energy Saver Topology for Delta Connected Induction Motors	IEE Proceedings - Electric Power Applications	Vol.152, No 4	pp.922-932	July 2005	1.11
K.Sundareswaran and A. Pavan Kumar	Voltage Harmonic Elimination In PWM AC Chopper Using Genetic Algorithm	IEE Proceedings - Electric Power Applications	Vol. 151, No. 1	pp.26-31	January 2004	0.75
K.Sundareswaran and M.Chandra	An Evolutionary Approach for Line Current Harmonic Reduction in AC/DC Converters	IEEE Transactions on Industrial Electronics	Vol.49, No.3	pp.716-719	June 2002	1.15
.K.Sundareswaran	An Improved Energy Saving Scheme for Capacitor Run Induction Motor Drive	IEEE Transactions on Industrial Electronics	Vol.48, no.1	pp.238-240	February 2001	1.09
.K.Sundareswaran and P.S. Manujith	Analysis and Performance Evaluation of Triac-Voltage Controlled Capacitor Run Induction Motor	Electric Power Components and Systems	Vol.32, No.9	pp. 913-925	September 2004	0.41
K.Sundareswaran and D.Laxminarayana	An Evolutionary Approach for Speed Controller Design of A.C.Voltage Controller Fed-Induction Motor	Electric Power components and Systems	Vol.30, No.10	pp.1001-1014	October 2002	0.25

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

	Drive					
.K.Sundareswaran	Line Current Harmonic Elimination and Voltage Control of PWM AC/DC Converter Using Hybrid Genetic Algorithm	Electric Power Components and Systems	Volume 35, Issue 2,		2007.	0.60
K.Sundareswaran and S.Razia Begum	Genetic Tuning of Power System Stabilizer	European Transactions on Electrical Power	Vol.14, No.3	pp.151-160	May-June, 2004	0.46
K.Sundareswaran, Javajisunil Babu, P.Kaviarasu, and Ramakrishna Ayinapudi	Dynamic Modeling And Simulation Of Thyristor Converter Fed DC Motor Drive	AMSE Journal (France) on Modeling, Measurement and Control	Vol.74, No.5, 6	pp.29-38	2001	0.123
K.Sundareswaran and S.Palani	Optimal Efficiency Control Of Induction Motor Drive Using Neural Networks	AMSE Journal (France) on Advances in Modeling and Analysis	Vol. 41, No.1	pp.9-21	1999	0.1
K.Sundareswaran and S.Palani	A Novel Technique For Sensor Less Speed Estimation Of Variable Voltage Induction Motor Drive Via Neural Networks	AMSE Journal (France) on Advances in Modeling and Analysis	Vol.40, No.1,2	pp.19-29	1998	
.K.Sundareswaran	Steady State Analysis and Simulation of PWM Inverter Fed Capacitor Run Induction Motor	IETE Journal of Research	Vol. 51, No. 6	pp. 441-445	November-December 2005	0.11
.K.Sundareswaran and A.Pavan Kumar,	“Performance Enhancement of PWM AC Chopper Using Random Search Method”,	Journal of Institution of Engineers (India) Electrical Division,	Vol.85,	pp.228-230,	March 2005.	0.20

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

K.Sundareswaran	A Simplified Model for Speed Control of A.C.Voltage Controller Fed Induction Motor Drives	IETE Journal of Research	Vol 49, No.4	pp. 247-250	July-August, 2003	0.38
K.Sundareswaran and P.S. Manujith	Analysis and Simulation of Phase Controlled Capacitor Run Induction Motors	Journal of Institution of Engineers (India)-Electrical Division	Vol.83,	pp.110-112,	September 2002.	
K.Sundareswaran and P.S. Manujith	Steady-State Analysis And Simulation Of A.C. Chopper Fed Capacitor Run Induction Motors	IETE Journal of Research	Vol 47, No.6	pp. 311-314	Nov-Dec, 2001	

(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numbers	Conference Theme	Venue	Year
K. Sundareswaran, V. Vigneshkumar, Sishaj P Simon and P. Srinivasa rao Nayak	Gravitational search algorithm combined with P&O method for MPPT in PV systems	IEEE INDIA CONFERENCE (INDICON)	Accepted for presentation		IISc Bangalore	December 16-18, 2016
K. Sundareswaran, V. Vigneshkumar, Sishaj P Simon and P. Srinivasa rao Nayak	Cascaded Simulated Annealing/Perturb and Observe method for MPPT in PV systems	IEEE International Conference on Power Electronics, Drives and Energy Systems (PEDES)	Accepted for presentation		Trivandram, Kerala	December 14-17, 2016
K.Sundareswaran, Kuruvinashetti Kiran and P. Srinivasa Rao Nayak	Application of Particle Swarm Optimization for Output Voltage Regulation of Dual Input Buck-Boost Converter	Second International conference on ICGCCEE-14			Coimbatore, Tamil Nadu	March 6-8, 2014

National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members

Sundareswaran, K. Kiran Kumar, I. Gangadhar, P.Sankar, P.S.Nayak and V. Vignesh,	Output Voltage Control and Power Management of a Dual Input Buck –Boost Converter Employing P&O Algorithm	Third International Conference on ACODS				March 13- 15, 2014.
K.Sundareswaran, K. Kiran Kumar, Hari Prasad, P.Sankar, P.S.Nayak and V. Vignesh	Optimization of Dual Input Buck Converter Control through Genetic Algorithm	Third International Conference on ACODS				March 13- 15, 2014.
K.Sundareswaran, Hariharan B, Daniel Sanju Antony, Fawas P P, Binyamin Subair, O V Asokan,	Optimal Placement of Static VAR Compensator using Particle Swarm Optimisation	International Conference on Power, Control and Embedded Systems (ICPCES-2010)			MNNIT Allahabad	Nov.28- Dec.1, 2010.
K.Sundareswaran, P. Bharathram, M. Siddharth, G. Vaishnavi, Nithin Anand Shrivastava and Harish Sharm	Voltage Profile Enhancement Through Optimal Placement of FACTS devices using Queen Bee Assisted GA	Third International Conference on Power systems (ICPS-2009)			IIT Kharagpur	December 27-29,2009
K.Sundareswaran and V.T.Sreedevi	Speed Regulator Design for a Thyristor- Voltage Controlled Induction Motor Drive using Bees Colony Intelligence	TIMA conference			Anna University, Chennai	Jan. 05-07, 2009.
K.Sundareswaran, H.N.Shyam, S.Palani and Joby James	Induction motor Parameter Identification Using Hybrid Genetic Algorithm	Proceedings IEEE Region 10 Colloquium and the Third ICIS			IIT Kharagpur	December 8-10,2008
K.Sundareswaran,	A Genetic	Proceedings IEEE			IIT Kharagpur	December

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

H. N. Shyam, Shan Abraham, G. Varakumar, Shashikant Kaul and R. Sheeba	Algorithm Based Approach towards Induction motor starting with minimum torque pulsations	Region 10 Colloquium and the Third ICIIS				8-10,2008
K.Sundareswaran, K.V.S.Manoj Kumar Vadali, Shaik Khaled Nadeem, H.N.Shyam	Robust Controller Identification for a Boost Type DC-DC Converter Using Genetic Algorithm	Proceedings IEEE Region 10 Colloquium and the Third ICIIS			IIT Kharagpur	December 8-10,2008
K.Sundareswaran and V.T.Sreedevi	D.C. Motor Speed Controller Design through a Colony of Honey Bees	IEEE International Conference, TENCON-2008	pp.1-6,		Hyderabad,	Nov. 18-21, 2008.
K.Sundareswaran and V.T.Sreedevi	Development of Novel Optimization Procedure Based on Honey Bee Foraging Behaviour	IEEE International Conference on Systems Man Cybernetics (SMC2008)	pp. 1220- 1225		Singapore	Oct. 12-15, 2008
K.Sundareswaran, N.Rengarajan, S. Palani, K.Abdul Hameed	Analysis on the Application of Soft Computing Methodologies for the Design of Power System Stabilizer	Proceedings of International conference, TIMA-2007	pp. 275- 280			Jan.2007
K.Sundareswaran, Bos Mathew Jose	Analysis, Simulation and Performance Comparison Of AC Voltage Controlled Fed Three Wire and Four Wire Connected Induction Motor Drives	Proceedings INDCON 2005, An international conference on IEEE India Council	pp. 133- 136		IIT, Chennai	December 2005

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

K.Sundareswaran, Bos Mathew Jose	Comprehensive Study On Starting Performance Of Thyristor Controlled Induction Motor Drives	Proceedings of the international conference on Computer applications in Electrical Engineering,	pp.528-531		Indian Institute Of Technology, Roorkee	September – 2005
K.Sundareswaran, N. Rajasekar	Harmonic Elimination in PWM AC Chopper Using a Hybrid G.A	Proceedings of the international conference on Computer applications in Electrical Engineering	pp.266-269		Indian Institute Of Technology, Roorkee	September - 2005
K.Sundareswaran, R.Madhan, C.V.N.Harish, and Krishna Jayanth	Development Of An Immigrant Genetic Algorithm For Enhanced Computational Efficiency And Its Application To Power Quality Improvement		pp.151-153	IEE International Conference on Energy, Information Technology and Power Sector (PEITSIC ON 2005)	Kolkata	Jan.28-29, 2005
K.Sundareswaran, C. Palaniappan, M.D. Anand, and R. Lingesham	Performance Evaluation Of Genetic Algorithm & Conventional Optimization Techniques To The Design Of Feedback Controller For Thyristorised Induction Motor Drives	Proceedings of international conference on Intelligent signal processing and robotics			Indian Institute of Information Technology, Allahabad	Feb. 20-23, 2004

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

K.Sundareswaran and S.Palani,	Design Of High Gain Controller For Part-Load Performance optimization of A.C. Voltage Controller Fed Induction Motor Drive	Proceedings of International Conference on Energy, Automation and Information Technology	pp.716-719		IIT, Kharagpur	December 2001
K.Sundareswaran,	High Performance AC Voltage Controller Fed Induction Motor Drive Using Fuzzy Logic Estimator	Proceedings of the 2000 International Power Electronics Conference			Tokyo, Japan,	April 2000
K.Sundareswaran and M.Vasu	Genetic Tuning of PI Controller for Speed Control of DC Motor Drive	Proceedings of IEEE International Conference on Industrial technology (ICIT-2000),	vol.1, pp.521-525		Goa	January 2000
K.Sundareswaran and S.Palani	Performance Enhancement of AC Voltage Controller Fed Induction Motor Drive Using Neural Networks	Proceedings of IEEE International Conference on Industrial technology (ICIT-2000)	Vol.1, pp.735-740		Goa	January 2000
K.Sundareswaran. and S.Palani	Design Of High Gain Controller For Part Load Performance Optimization Of Variable Voltage Induction Motor Drive	Proceedings of 3rd IEEE International conference on Power Electronics and drive systems,	Vol.1, pp.273-275,		Hong Kong	July 1999.
K.Sundareswaran and S.Palani	Fuzzy Logic Approach For Energy Efficient Voltage Controlled Induction Motor Drive	Proceedings of 3rd IEEE International conference on Power Electronics and Drive systems	Vol.1, pp.552-554,		Hong Kong	July 1999.
K.Sundareswaran and S.Palani	Artificial Neural Network Based	Proceedings of IEEE Region 10	pp.410-413		New Delhi	December 17-19, 1998

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

	Voltage Controller for Energy Efficient Induction Motor Drive	International Conference				
K.Sundareswaran and S.Palani	Speed Identification Of Voltage Controlled Induction Motor Drive Using Neural Networks	Proceedings of International Conference on computer applications in Electrical Engineering, University of Roorkee	pp.77-80			September 1997
K.Sundareswaran	Development of An Intelligent Optimization Algorithm Throughh a colony of Foraging Ants and its Application to D.C.Motor Speed Controller Design	Proceedings of National Conference on Electrical Engineering and Embedded systems	pp. 1-4	NCEEE '08	Anna Univsersity Chennai	20-21 March 2008
K.Sundareswaran, Krishna Jayant, C.V.N.Harish and R.Madhan	Optimum Feed Back Controller Design For A Non-Linear Plant Using The Ant Colony Metaphor	Proceedings of USC-SAP(DRS) National Conference On Computing and Mathematical Modeling			Gandhigram Rural Institute, Gandhigram, Tamilnadu.	March 2005,
Sundareswaran, S.Hemamalini, T.N.Shanavas and R.Madhan	Parameter Estimation Of Unknown Plants Using Particle Swarm Optimization	Proceedings of USC-SAP (DRS) National Conference On Computing and Mathematical Modeling			Gandhigram Rural Institute, Gandhigram, Tamilnadu	March 2005
Sundareswaran and N.Rajasekar	A Comprehensive Study Of Capacitor-Run Induction Motor Speed Control with Integral	Proceedings of 13-th National Power Systems Conference (NPSC 2004)	pp.1002-1005		IIT Chennai	Dec.27-30, 2004

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

	Switching					
Sundareswaran, Bos Mathew Jos and V.T.Sreedevi	Dynamic Simulation Of AC Voltage Controller Fed- Induction Motor Drive using simulink	Proceedings of AICTE sponsored National conference on Control, Communication and Information Systems	pp.154- 157		Goa	Jan. 23-24, 2004
K.Sundareswaran	A Quantitative Study of Performance Characteristics of Variable Voltage Operation of Domestic Fans	Proceedings of 1st National conference on instrumentation & control			National Institute of Technology, Tiruchirappalli	December 5 & 6, 2003,
K.Sundareswaran, Bos Mathew Jos	Transient Response Analysis of Delta Connected Induction Motor Starting Using Star-Delta Switch	Proceedings of PCIC-2004		National C onference on Power Conversion and Industrial Control		July, 2004
.K.Sundareswaran, M.D.Anand and C.Palaniappan	Modeling And Simulation Of DC -Fed Braking Of Induction Motor Drive	Proceedings of PCIC-2003		National C onference on Power Conversion and Industrial Control		January 3-4, 2003
Sundareswaran, M. D. Anand and Arun P Anand	FPGA Based IC Design For Performance Enhancement Of Delta Connected Induction Motor Drives	Proceedings of PCIC-2003		National C onference on Power Conversion and industrial Control		January 3-4, 2003
K.Sundareswaran,	Transient Simulation Of Reversible DC Drive Using Power System Block Set	Proceedings of 25th National Systems conference	pp. 44-47		PSG Tech	December 2001
K.Sundareswaran, Javaji Sunil Babu,	Application of Genetic	Proceedings of the National	pp.89-94		Anna university,	February 2000

**National Institute of Technology, Tiruchirappalli:
Performa for CV of Faculty/ Staff Members**

Sambath Hemanth Kumar and Ramakrishna Ayinapudi	Algorithm for Speed Control of DC Motor Drive	symposium on Intelligent Measurement and Control			Madras	
K.Sundareswaran and S.Palani	Energy Efficient Induction Motor Drive Using Neural Networks	Proceedings of National Renewable Energy Convention-99	pp.no.173-177		Indore	December 1999
K. Sundareswaran and S.Palani	Minimum-Time Maximum-Efficiency Control Of Voltage Controlled Induction Motor Drive	Proceedings of 22nd National Systems Conference	pp.392-396		REC Calicut	December 11-13, 1998
.K.Sundareswaran and K.Madhukar	Digital Simulation And Experimental Results Of Energy Efficient Voltage Controlled Single-Phase Induction Motor	Proceedings of National Conference on electric drives and control for Transportation systems at Vidisha	pp.459-468			January 1997

(C) Books & Monographs

Author(s)	Title of Book/Monograph	Name of Publishers	Year of Publication	ISSN/ISBN Number
Dr.K.Sundareswaran	A Learner's Guide to Fuzzy Logic Systems	Jaico Publishing House	2005	ISBN 81-7992 – 416 - 5

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