Experimental Setup of MB-OFDM UWB System:

Fig. 1(a) FPGA board and Data Converter boards

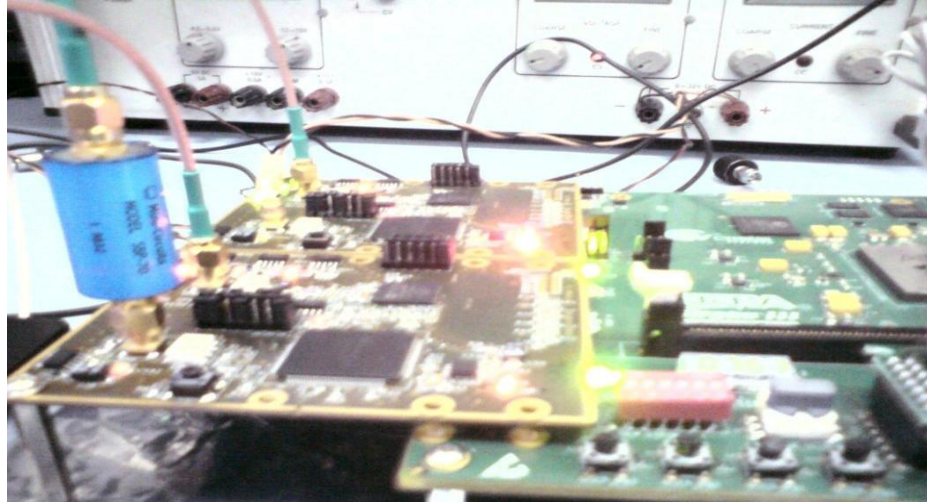Fig. 1(b) FPGA board and Data Converter boards

Fig. 2 Showing the interconnection between FPGA and DATA Converters
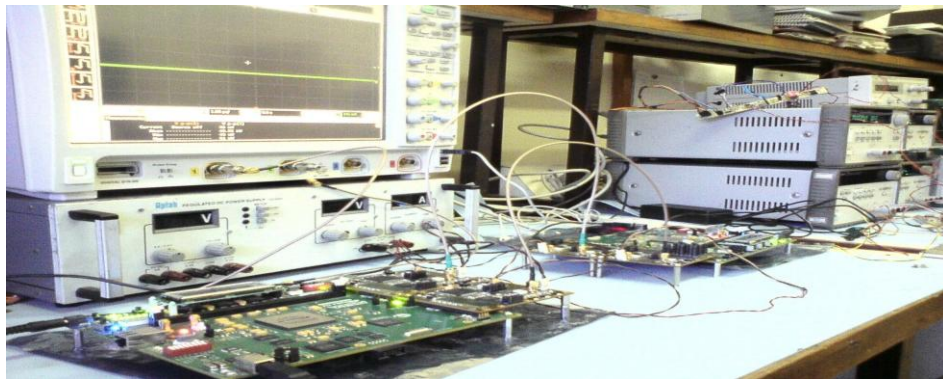

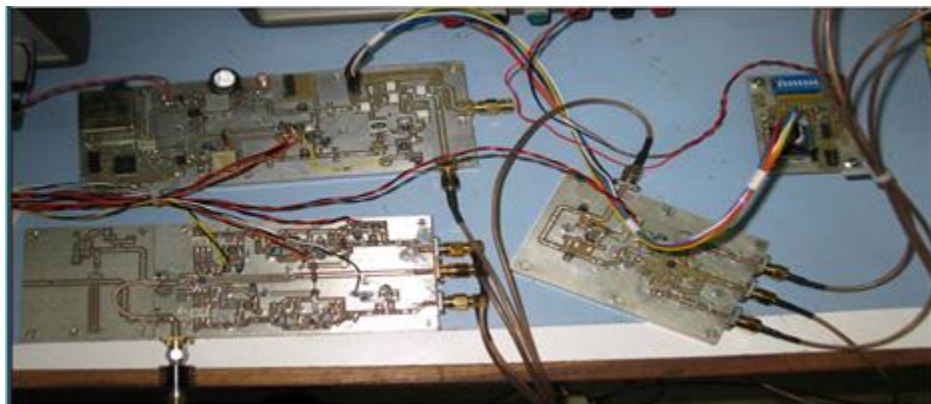Fig. 3 Two different FPGA boards used for testing MB-UWB System

Fig. 4 RF front end boards

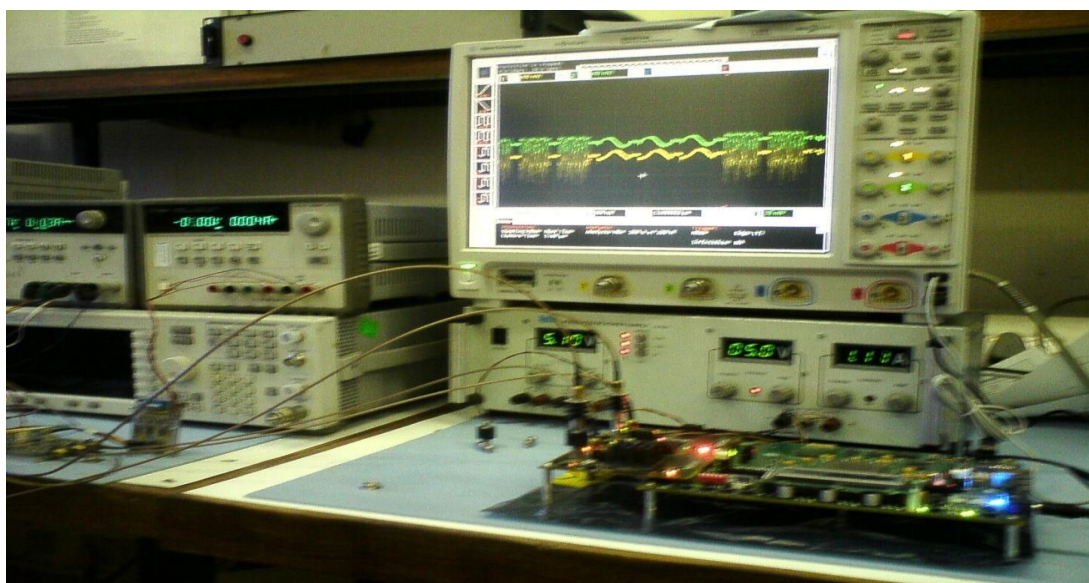Fig. 5 Integration of RF front end boards with Digital backend board including ADC & DAC boards.

Fig. 6 Showing output of RF Receiver of MB-OFDM UWB System