Digital Signal Processors
Architecture, Programming and Applications

Second Edition

B Venkataramani
M Bhaskar

This text provides a thorough understanding of the architecture and programming of Digital Signal Processors. It blends the concepts of digital signal processing with its applications on systems using digital signal processors. This revised edition offers an enhanced coverage of TMS320C6X series of processors and FPGA based system design— emerging trends in Digital Signal Processors.

Salient Features:
- New chapters on
  - Instruction set and application programs of TMS320C6X
  - Architecture and instruction set of TMS320C55X
  - FPGAs in telecommunication applications
- Discusses a wide variety of Texas Instruments (TI) Digital Signal Processors including C3X, C5X, C54X, C55X and C6X
- Strong emphasis on the application of each of the Digital Signal Processors
- Application of the CODE COMPOSER STUDIO software for design and testing of DSP based systems
- Rich Pedagogy:
  - Solved examples: 147
  - Review questions: 357
  - Assembly Language Programs: 35
  - Objective type questions: 341

http://www.mhhe.com/venkataramani/dsp2