Centre for Electronics System Design, Calibration and Testing
NATIONAL INSTITUTE OF TECHNOLOGY , TIRUCHIRAPALLI

One Week Hands-on Workshop on

SEMICONDUCTOR DEVICE CHARACTERIZATION & PARAMETER EXTRACTION

14th December 2026 – 18th December 2026

Organized by

Centre for Electronics System Design, Calibration and Testing (CESDeCT)

Scan to register

About the Institute
The National Institute of Technology, Tiruchirappalli (NIT Trichy), is one of India's premier institutions of national importance. It is widely recognized for its academic excellence, cutting-edge research, innovation, and strong industry collaboration. The institute remains committed to developing future-ready engineers and driving technological advancements for societal impact.

About the Department
The Department of Electronics and Communication Engineering (ECE) at NIT Trichy was established in 1968, and launched its M.Tech. in VLSI System program in 1999. As a leading Centre for education and research, the department offers state-of-the-art facilities in VLSI, embedded systems, signal processing, wireless communication, and AI hardware. It consistently fosters innovation through interdisciplinary research and strategic industry partnerships.

About the Centre – CESDeCT
The Centre for Electronics System Design, Calibration and Testing was established in 2021 within the Department of Electronics and Communication Engineering (ECE). Funded with ₹145 Lakhs from the Higher Education Financing Agency (HEFA) under the Interdisciplinary Research Centre and Labs (IRCL) initiative, the Centre features advanced equipment for semiconductor, IC, and RF characterization, as well as antenna fabrication and testing up to 6 GHz.

About the Workshop
A five-day, hands-on training programme covering the characterization of semiconductor passive devices, MOSFETs, and HEMTs. The course features practical sessions on I-V and C-V measurements, as well as the use of RF sources, spectrum analyzers, and VNAs. Participants will also learn calibration techniques, LNA design, and parameter extraction. Sessions will be conducted by NIT Trichy faculty and industry experts from Tektronix.

Target Audience
UG/PG students/Research scholars, Staff members, Faculty members and Industry professionals interested in Semiconductor devices, RF devices and measurement.

Resource Persons
- Dr. Guruprasad G, Assistant Professor, ECE, NIT Trichy
- Dr. P Surya Teja Naga Srinivas, Assistant Professor, ECE, NIT Trichy
- Dr. Manikandan R.R. , Assistant Professor, ECE, NIT Trichy
- Dr. M. Bhaskar, Professor, ECE, NIT Trichy
- Industry experts from M/s Tektronix
- OTHER EXTERNAL EXPERTS from Academia and Industry

Coordinators
- Dr. Guruprasad G, Assistant Professor, ECE, NIT Trichy
- Dr. P Surya Teja Naga Srinivas, Assistant Professor, ECE, NIT Trichy
- Dr. Manikandan R.R. , Assistant Professor, ECE, NIT Trichy

Facilitator
- Dr. M. Bhaskar, Professor, ECE, NIT Trichy

Workshop Highlights
★ Hands-on Lab Sessions | ★ I-V & C-V Measurements | ★ RF & VNA Calibration | ★ MOSFET & HEMT Characterization

How to apply

Registration link : https://docs.google.com/forms/d/e/1FAIpQLSe8fHEdmdSk_dQA07uVJy-mwINqzFt2k_h2jW39dvSjupVwbg/viewform
Last date for registration : 7th December 2026
Registration fee : Rs. 7000/- (Student), Rs. 8100/- (Staff), Rs. 11000/- (Faculty)
Rs. 15800/-(Industry Professional)
(Accommodation at Hostel/Guest House and food will be extra as per our Institute norms)

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