

VRITIKA (Training and Skill Internship)

'VRITIKA' is the call for initiation and practice in science through the Training and Skill Internship. This program aims to provide opportunities to the promising PG students from universities and colleges to get exposure and research skill development experience. These internships will primarily be facilitated by organizations/institutions/laboratories of national importance such as IITs, IISc, IISERs, NITs, CSIR, ICAR, ICMR etc. For more details, visit the following links: <https://acceleratevigyan.gov.in/programs/abhyas/vritika> & <http://serb.gov.in/abhyaas.php>

Call for Research Internship (Accelerate Vigyan - Vritika) – Physical Mode

Title: Design and Implementation of RISC-V core for Fault-Tolerance Network-on-Chip

Who can Apply:

- ❖ **Regular PG level (MTech/M.E./M.S)** students pursuing their degree from AICTE approved institution within India are eligible to apply.
- ❖ Specialization – Embedded Systems, VLSI based system design On-Chip networks and FPGA Implementations.
- ❖ The applicants must produce a letter of authentication from their Supervisor/Head of the Department/Head of the Institute indicating their association with the Institute and a "No Objection Certificate (NOC)" for allowing their student to undergo training in the workshop if selected. There is no dedicated format for the same; however, it must be obtained on the institute/university letterhead.
- ❖ Students who have demonstrated strong research and analytical skills in previous professional and academic work would be given preference.

How to Apply:

- ❖ **Step 1:** The candidate shall apply for the Internship program by filling out the following Google Form:
<https://forms.gle/GPwWLWZWFYSYmQLaA>
- ❖ **Step 2:** Please fill the above Google form with the requested details and upload the scanned copies of the certificate and no objection certificate (from HoD/Head of the institution) by **6th May, 2023**
- ❖ **Step 3:** Take the printout of the filled Google form (received in your registered email) and sign at the bottom of all pages.
- ❖ **Step 4:** E-mail the scanned copy of the signed registration form to bnkreddy@nitt.edu by **6th May, 2023**.
- ❖ The list of shortlisted students for the written test/interview shall be announced on or before **8th May 2023**.
- ❖ Tentative date of written test/interview (online mode): **8th May 2023**.

Details of Internship & Selection Process

- ❖ Duration – **15th May 2023 to 29th June 2023 (6 Weeks)**
- ❖ Venue: **Department of Electronics and Communication Engineering, National Institute of Technology Tiruchirappalli, Tamil Nadu-620015.**
- ❖ Number of Interns Required – **05**
- ❖ Last Date for application/registration: **6th May, 2023**
- ❖ List of selected students for the internship shall be announced on or before **10th May 2023**.
- ❖ Reporting date for the selected students – **15th May 2023 (NIT Tiruchirappalli)**.
- ❖ **No stipend will be provided for the internship.**

Selection process:

- The registered candidates shall be shortlisted for a written test or interview based on the eligibility criteria and academic performance/GATE score.
- The shortlisted candidates shall be selected for the research internship program based on their performance in the written test/interview.

Accommodation:

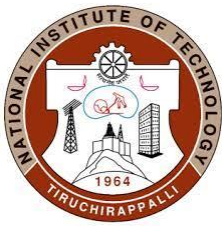
- Selected interns will be accommodated in the Institute guest house/hostel rooms (if available) with catering facilities.
- A limited number of rooms in NITT Guesthouses/Hostels are available on First Come, and First Served basis. THE HOST INSTITUTION SHALL BEAR the boarding and lodging within the NITT premise only. If unavailable, the participants need to make self-arrangements for their stay outside the NITT premise, which the organizers do not bear. TA will be reimbursed for the train or bus lowest fare (from home institute/hometown to NITT and vice versa).

WHAT'S IN IT FOR YOU?

- You will get a chance to learn and enhance your skills in VLSI and its application to On-Chip networks (including architectures, simulation, algorithm/hardware development and control, technical writing, etc.).
- This program would facilitate the young talent to choose a career path in Science and Technology at a later stage.
- Certificates will be provided to the interns after the successful completion of the internship.

ADDRESS FOR
CORRESPONDENCE:

**Dr. B. Naresh Kumar Reddy, Assistant Professor, ECE Department,
National Institute of Technology Tiruchirappalli,
Tiruchirappalli, Tamil Nadu-620015
Email: bnkreddy@nitt.edu
Contact Number: +91- 9966539090**



Call for Research Internship (Accelerate Vigyan - Vritika)

Title: Design and Implementation of RISC-V core for Fault-Tolerance Network-on-Chip

DECLARATION FORM

1. Name (In Block Letters):
2. Date of birth:Gender:
3. Category (M.Tech/M.E/M.S./Ph.D. student):
4. Institution:
5. Department:
6. Mobile:
7. e-mail:
8. Specialization:
9. Accommodation is required (Yes/No).....
10. Official Address:

Declaration: The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the program and shall attend the course for the entire duration.

Name & Signature of the candidate

No Objection Certificate (NOC) from Project Supervisor/HoD/Head of Institution

I hereby certify that Mr./Ms. is a (M.Tech/M.E/PhD) student of I have no objection to him/her undergoing a research internship (if selected) on “**Design and Implementation of RISC-V core for Fault-Tolerance Network-on-Chip**” at the National Institute of Technology Tiruchirappalli, Tamil Nadu, from **15th May 2023 to 29th June 2023**.

Place:

Name & Signature of Project Supervisor/HoD/Head of Institution

Date:

(Department/Institute Seal)