5-Days Online Workshop on
FPGA Based SoC Design Targeted for
Implementing AI/ML Algorithms

27 – 31 December 2021

Jointly Organized by
Centre for System On-Chip (SoC) Design and Fabrication
Department of Electronics and Communication Engineering
&
Department of Computer Applications
National Institute of Technology Tiruchirappalli,
Tamil Nadu, India
About the Programme

Field-programmable gate array (FPGA) chips enable you to reprogram logic gates. You can use FPGA technology to overwrite chip configurations and create custom circuits. FPGA chips are especially useful for machine learning and deep learning. For example, using FPGA for deep learning enables you to optimize throughput and adapt processors to meet the specific needs of different deep learning architectures.

Focus Areas

- Introduction to Machine Learning & Deep Learning
- Convolutional Neural Network
- Computing Convolutional
- Reducing Complexity
- DL Acceleration Landscape
- Hardware Design Flow
- Software Design Flow using Vitis
- Platform Project Creation using Vitis
- Application Project Creation using Vitis
- Debugging using Vitis
- QEMU: Hardware Emulator
- Network on Chip
- Demo: Realization of simple NoC on FPGA
- Demo: Realization of basic image processing algorithms on FPGA

Target Audience

- Academicians, Research scholars, PG Students & Industry personnel.

Resource Persons

- The course faculty includes resource persons from NITs and other reputed industries like Xilinx, Coreel.

Important Dates

Last Date of Online Registration: 21st December 2021

Registration Fee: Rs. 1000/- (Including 18% GST)

The number of participants is restricted to 75.

Timing: 10AM-12PM & 2PM-4PM

How to Apply

Apply Online Through the Following Link

https://forms.gle/ETm293uVeb7HoyWn9

Payment of Registration Fee

Go to the SBI-collect using the following link

https://www.onlinesbi.com/sbicollect/icollecthome.htm

Select the state as ‘Tamil Nadu’, and category as ‘Educational Institutions’.

Select “Conference and Workshop NIT Trichy”.

Select payment category as “FPGA ECE 2021”.

Make payment through UPI/ Net Banking/ Credit card/ NEFT.

Once the fee is paid, send the payment receipt to cfsocnitt@gmail.com

Address for Communication

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