

**NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI- 620 015.**  
**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

**Date: 24-03-2017**

**LIST OF CANDIDATES SHORTLISTED FOR WRITTEN TEST AND INTERVIEW**

<b>Title of the Project</b>	:	<b>Design and Development of Solar Photo- Voltaic Powered Cold Storage System</b>
<b>Project Positions</b>	:	<b>JRF - 02 Nos. (Electrical-1, Mechanical-1)</b>
<b>Funding Agency</b>	:	<b>DST-CERI - Habitat Energy Efficiency</b>
<b>Written test and Interview</b>	:	<b>Date : 29/03/2017</b> <b>Time : 08.30 A.M. (Written Test) and 11.00 A.M. (Interview)</b> <b>Venue : EEE Department, NIT-Trichy.</b>

**ELECTRICAL AND ELECTRONICS ENGINEERING**

<b>S.NO.</b>	<b>NAME</b>
1.	AROBINDA DASH
2.	S. SINDHUJA
3.	KESAVAN.T
4.	MANIKANDAN.N
5.	T.M BASKAR
6.	K.INDUMATHI
7.	HARIHARAN.S
8.	CHANCHAL S CHANDRAN
9.	SHEEBA.T.A
10.	R.SARAVANAN
11.	C.GNANAVEL
12.	N.BABU
13.	V.VEERAMANI
14.	RITU SINGH
15.	ANITHA.P
16.	PAN CHSHEELHARIGORA

**MECHANICAL ENGINEERING**

<b>S.NO.</b>	<b>NAME</b>
1.	AMRIT KUMAR THAKUR
2.	THANGA GURUSAMY.B
3.	NAVEEN KUMAR NAGALLI
4.	R.VENKATESH KUMAR
5.	SARAFOJI.P
6.	P.PAVENTHAN
7.	S.PUNITHAN
8.	MANOJ J K
9.	SINDHUJA P
10.	SUDHARSHAN.K
11.	GOUTHAMAN.J
12.	PRAVEEN MARUTHUR