



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI – 620015**

Ref.: NITT/ECE/RES/SAR-ADC/JRF/Interview notice

Date: 10-12-2019

Provisional list of candidates short listed for written test/ interview to be held on

16-12-2019 at 11 AM in the VLSI laboratory, ECE, Department, NIT, Trichy

S. No.	Name	Mail id
1.	Archana S	archa.vrindavan@gmail.com
2.	Bomma Sandhya Rani	sandyarani.bomma@gmail.com
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8.	Sowmiya	sowmiyaece83@gmail.com
9.	Avijit Patra	avikabita@gmail.com
10.	Shanmathi.S	sshanmathi304@gmail.com
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12.	Supreem H	supreem600@gmail.com
13.	Sivaraju	usiva10@gmail.com
14.	Sathish V	sathishpv792@gmail.com
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16.	Ramya M	ramyamohan094@gmail.com
17.	Avanesh Kumar A	avaneshkumar5594@gmail.com
18.	Sangeetha K	sangee.saratha@gmail.com

Note: These candidates are required to submit the softcopy of the community certificate and GATE score card in advance by mail to vlsilabnitt@gmail.com and their originals in person at the time of written test/interview. They would be allowed to attend the written test/interview only if they have qualified in the GATE exam.

Coordinator, ISRO respond project on SAR ADC