



**Department of Electronics and Communication Engineering**  
**National Institute of Technology**  
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December 21, 2020

**Notice of Ph.D. Viva-Voce**

**Subject: Ph.D. Viva-Voce of Mr. Phate Vikas Ravindra – Full-Time Ph.D. Scholar under QIP(Poly),  
Department of Electronics and Communication Engineering – Reg.**

Mr. Phate Vikas Ravindra (408117008) Full-Time Research Scholar under QIP(Poly) scheme, Department of Electronics and Communication Engineering will defend his Ph.D. thesis in a Public Viva-Voce (Online mode) as per the regulations of the Ph.D program of National Institute of Technology, Tiruchirappalli.

The schedule of Viva-Voce examination is as follows:

Date : January 06, 2021


Time : 3:00 PM

Title of the Thesis : **Implementation of Computer Vision Based System for Mass Modeling of Sweet  
Lime Fruits**

Mode : Online (video conferencing using CISCO Webex meetings)

The link details are to be communicated later.

All are cordially invited to attend the Viva-Voce and take part in the discussion. A copy of the thesis is available in the department for reference.

     
**(Dr. R. Malmathanraj) (Dr. P. Palanisamy) (Dr. B. Malarkodi) (Dr. P. Muthuchidambaramanathan)**

Research Supervisor

Research Co-Supervisor

DC Chairman

H.O.D. (ECE)

Copy to :

- 1) The Director, NIT, Trichy for kind information
- 2) The Dean (Academic)/Associate Dean (MS/PhD), NIT-T
- 3) All the Department H.O.Ds with a request to circulate in the department
- 4) The Examiner and Doctoral Committee members
- 5) All Faculties of NIT-T
- 6) The Registrar and Librarian, NIT-T