### **Curriculum Vitae**



- 1. Name:
- 2. Designation:
- 3. Office Address:

### B. SHAMEEDHA BEGUM ASSISTANT PROFESSOR DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

- 4. Telephone (Direct) (Optional): Telephone : 04312503215 Extn (Optional): Mobile (Optional):
- 5. Email (Primary):shameedha@nitt.edu
- 6. Field(s) of Specialization:
- 7. Employment Profile

Email (Secondary) : COMPUTER ARCHITECTURE

Job Title	Employer	From	То
Assistant Professor	NIT, Trichy	01-12-2008	Till Date
Programmer Analyst	Cognizant Technology Solutions,Coimbatore	01-07-2007	30-11-2008
Lecturer	CIT,Coimbatore	1-6-2004	30-8-2005

8. Academic Qualifications (From Highest Degree to High School):

Examination	Board / University	Year	Division/ Grade	Subjects
Ph.D.	NITT	2019	-	Cache Memory
M.E.	ANNA UNIVERSITY	2007	Ist - Distn.	Embedded And Real Time Systems
B.E.	AVINASHILINGAM DEEMED UNIVERSITY	2004	I Class	Computer Science

9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Ce ntre/Institution	From	То
Grievances of the OBC & PWD	NITT	17-09-2021	Till Date
Minority Employees Committee			
Member			
Member-Online Degree Sub	NITT	09-08-2021	Till Date
Committee			
Faculty Advisor- Environmental	NITT	28-07-2021	Till Date
Club			
Member-Library Advisory	NITT	08-12-2020	Till Date
Committee			
Faculty Advisor-IEEE SB-	NITT	01-07-2020	30/10/2021
COMPUTER SOC			
Staff Advisor-Pragyan	NITT	01-07-2017	2018-05-31
Faculty Advisor-Vortex	NITT	01-07-2015	2015-05-31
Member- CSG	NITT	29-05-2009	2011-03-30

#### 10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	То
Training Programe-	IMTI-Tamil Nadu	2009	-
Expert			
Inspection	Anna University	2014	-
Committee Member			
MTech Project	IIIT Srirangam	2016	
Guidence			
Doctoral Committee	Coimbatore Institute of	2020	Till date
Member	Technology		
Doctoral Committee	SRMIST-Kattankulathur,Chennai	2020	Till date
Member			

11. Awards, Associateships etc. Received Prime Minister Research Fellowship for Kannadasan K (Research Scholar)

12. Fellowships:NA

- 13. Details of Academic Work
  - (i) Curriculum Development: Involved in BOS
  - (ii) Courses taught at Postgraduate and Undergraduate levels:
    - Advanced Microprocessor Systems
    - Numerical Computing
    - Microprocessor Systems
    - Logical Foundations of Computer Science
    - Digital Computer Fundamentals
    - Digital Systems Design(Theory and Lab)
    - Mobile Applications Development (Theory and Lab)
    - Parallel Computer Architecture
    - C Programming(Theory and Lab)
    - Introduction to Algorithms
    - Real Time Systems
    - Computer Organization
    - Computer Architecture
    - Parallel Architecture and Programming

(iii)Projects guided at Postgraduate level: 27 Projects

- (iv)Other contribution(s): Involved in various activities such as Faculty/Class advisor for B.Tech students, Time Table Incharge, NBA committee member, Pruchase Initiator, TEQIP- Dept., Coordinator, Project Committee member Etc.,
- 14. Details of Major R&D Projects

Title of Project	Funding Agonay	Dura	tion	Status
Title of Project	Funding Agency	From	То	Ongoing/ Completed
Energy Efficient	DST	04.05.2014	04.05.2014	Completed
Implementation				_
Of Multi Modular				
Exponential				
Techniques For				
Public Key				
Crypto Systems				
Wireless Seat Belt	Rane Group Of	01.08.2021	Till Date	Ongoing
Remainder	Industries			_

#### 15. Number of PhDs guided: 03(Ongoing)

16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National/ Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
11th May 2020 - 15th May 2020	Faculty Development Program on "E-Content Development"	National	Participant	NITTTRC, Chennai	NITT
26th February 2019	One Day Workshop on " Hardware Security"	National	Participant	SETS Chennai	SETS Chennai
2nd to 6th July 2018	One Week Summer School on "Computer Architecture"	National	Participant	IIT Kanpur	IIT Kanpur
11th - 16th September 2017	One Week Faculty Development Program on "Parallel Programming for HPC"	National	Participant	IISC Bangalore	IISC Bangalore
18th August 2015	One day Digital India Workshop	National	Participant	NITT	NITT
25th May 2015 to 30 th May 2015	Two Weeks ISTE SSTP on "Introduction to algorithms"	National	Participant	NITT	NITT
Dec 2010	Two Day workshop on "Intelligent Robotics "	National	Participant	IIT, Bombay	IIT, Bombay
Jun 29 - Jul 4 2009	One week workshop on	National	Participant	IIT Kanpur	IIT Kanpur

"Programme		
Optimization		
for Multicore		
Architectures"		

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
Five Days Online workshop on Cloud Computing and Big Data	National	May 17- 21, 2021	Co-Convenor	NITT
Five Days Online workshop on "Cyber Security and Ethical Hacking"	National	March 22 -26, 2021	Co-Convenor	NITT
Five Days FDP on "Smart Grid Features and Blockchain Technology for Smart Grid"	National	Nov 16- 20,2020	Co-Convenor	NITT
Five Days Short term course on "Deep Learning"	National	July 6 – 10, 2020	Co-Convenor	NITT
Conference on "Secure Reconfigurable Architectures And Intelligent Computing"	International	Nov 28-30, 2019	Co-Convenor	NITT
14 Days Computer Training for Police Personnel	National	June 13-24, 2018	Co-Convenor	NITT
Five days Workshop on "Xilinx SoC: FPGA Based Design"	National	July 30 –Aug 3, 2018	Co-Convenor	NITT
Two days Workshop on "Digital Design Using XILINX SDSoC Software Tool"	National	March 16-17, 2017	Co-Convenor	NITT
Four days Workshop on "Introduction to	National	Feb 24-27, 2017	Co-Convenor	NITT

Embedded Systems with Application to IoT"				
Two Days Workshop on "Advanced Embedded System Design on Zynq Using Vivado Targeting Zed Board"	National	Aug 28-29, 2015	Co-Convenor	NITT

#### 18. Invited Talks delivered

	Тој	pic	Date	Invi	ting Orga	anization
FDP	on	Embedded	03/03/2021 To 04/03/2021	College	of	Engineering,
Systems	5			Kottayam,	, Kerala	

#### 19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life	Organization	Membership No. with date
Member )		
Senior Member	IEEE	93849938

#### 20. Academic Foreign Visits

Country	Duration of Visit	Programme
London, UK	October 17-18,	International Conference on Internet of
	2017	Things and Machine Learning 2017

#### 21. Publications

#### (A) <u>Refereed Research Journals</u>:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal
Jobin Jose,Shameedha Begum, N Ramasubramanian	Effectiveness Evaluation of Replacement Policies for On- Chip Caches in Multiprocessors	IJERTCS	13	1-12	2021	(Optional)

	[	1	1	1	· · · · · · · · · · · · · · · · · · ·
Kokila J, Arjun	Hardware	Periodica	63	244–	2019
Murali Das,	Signature	Polytechnica		253	
Shameedha B,	Generation	Electrical			
Ramasubramanian	Using a Hybrid	Engineering and			
Ν	PUF and FSM	Computer			
	model for an	Science			
	SoC				
	Architecture				
Bhukya Krishna	Cache lifetime	Microelectronics	97	1-15	2019
Priya, Sampath	enhancement	Reliability			
Kumar, B	technique using	-			
Shameedha	hybrid cache-				
Begum, N	replacement-				
Ramasubramanian	policy				
B. Shameedha	Design of an	IJERTCS	10	87-107	2019
Begum, N.	Intelligent Data				
Ramasubramanian	Cache with				
	Replacement				
	policy				
Satyanarayana	Bit Forwarding	IJISP	11	11-24	2017
Vollala, B	3-Bits				
Shameedha	Technique for				
Begum, Amit D	Efficient				
Joshi, N	Modular				
Ramasubramanian	Exponentiation				
Shameedha	A	IJCTA	9	8509-	2016
Begum, T Vidya,	Reconfigurable			8517	
Amit D. Joshi,	Cache Design				
and N	for Embedded				
Ramasubramanian	Dynamic Data				
	cache				
Babysyla,	Defending	JCSEITR	5	7-20	2015
Satheesh Kumar	against				
and Shameedha	malwares:				
Begum	Sandbox				
	Detection and				
	Prevention of				
	Malwares in				
	Android				
	Devices				
	Devices				

Author(s)	Title of Abstract/ Paper	Title of the Proceedi	Page number s	Conference Theme	Venue	Year
		ngs				
AnupDas,	Studies On	Springer	255–	International	Online	2021
Bhukya	Performance Of		264	conference on		
Krishna Priya,	ImageSplicing			Computing in		
B. Shameedha	<b>Techniques Using</b>			Engineering &		
Begum,N.Ram	Learned Self-			Technology		
asubramanian	Consistency					
Krishna Priya,	Enhancing the	IEEE	1-6	International	Dhanba	2018
Sampath	Lifetime of STT-			Conference on	d	
Kumar, N.	RAM with MRU			Recent		
Ramasubrama	Replacement			Advances in		
nian, B.	Algorithm			Information		
Shameedha				Technology(R		
Begum		IFFF	201	AIT)	D1 1	2017
Amit D. Joshi,	Analyzing Multi- core Cache	IEEE	381 -	International	Bhopal	2017
Indrajeet S, N. Ramasubrama	Coherence		388	Conference on Recent		
nian, B.	Protocols from			Innovation in		
Shameedha	Energy and			Signal		
Begum	Performance			processing and		
Deguiii	Perspective			Embedded		
	reispective			System		
Satyanarayana	Evaluation of	IEEE	1-9	International	UK	2017
Vollala,	Password		- /	Conference on	011	
Indrajeet S, B.	Encrypted Key			Internet of		
Shameedha	Exchange			things and		
Begum, and	authentication			Machine		
N.	Techniques:			Learning		
Ramasubrama	Design approach			_		
nian	perspective					
Satyanarayana	High-Radix	IEEE	346-	International	Pune	2016
Vollala,	Modular		350	Conference on		
Shameedha	Exponentiation			Computing,		
Begum, Amit	for Hardware			Analytics and		
D. Joshi and	Implementation of			Security		
N.	Public-Key			Trends		
Ramasubrama	Cryptography					
nian D. Shawaa dha	Destant	IEEE	210	Turken (* 1	Der	2016
B. Shameedha	Design of a	IEEE	318-	International	Pune	2016
Begum;Arun	Reconfigurable		322	Conference on		
Krishnakumar;	Embedded Data			Computing,		
Amit D.	Cache			Analytics and		

Joshi;N. Ramasubrama nian				Security Trends		
Joshi, A. D., Vollala Satyanarayana ., Begum, B. S., & Ramasubrama nian N	Performance Analysis of Cache Coherence Protocols for Multi-core Architectures: A System Attribute Perspective	ACM	1-7	International Conference on Advances in Information Communicatio n Technology & Computing	Bikanir	2016
Satyanarayana Vollala, B. Shameedha Begum, and N. Ramasubrama nian	Hardware design for multiplicative modular inverse based on table look up technique	IEEE	520- 523	International Conference on Computing and Network Communicatio ns	Trivand rum	2015
Begum, B. Shameedha, and N. Ramasubrama nian	A comparative study of cache performance for embedded applications	IEEE	872- 876	International Conference on Computing and Network Communicatio ns	Trivand rum	2015

#### (C) Books Chapters

Author(s)	Title of	Name of	Year of	ISSN/ISBN
Tutiloi(3)	Book/Monograph	Publishers	Publication	Number
Diantana Katalana	υī			
Bhukya Krishna	<b>Applied Information</b>	SPRINGER	2021	978-981-
Priya, Anup Das,	Processing Systems			16-2007-2
Shameedha				
Begum & N.				
Ramasubramanian				
Ruchika	Computing in	SPRINGER	2019	978-981-
Janbandhu,	Engineering and			32-9514-8
Shameedha	Technology			
Begum & N.				
Ramasubramanian				
Satyanarayana	<b>Recent Findings in</b>	SPRINGER	2018	978-981-
Vollala, N.	Intelligent Computing			10-8638-0
Ramasubramanian,	Techniques			
B. Shameedha	_			
Begum & Amit D.				
Joshi				