

6 OCT 2015

ADVERTISEMENT

Applications are invited from post graduates with **M.E in Communication systems/Applied Electronics/VLSI Design** for the temporary post of research assistant.

- The selected staff would be required to work on the design of RF frontend for wireless sensor network
- The applicants are expected to have a sound knowledge of analog circuit design technique and would be required to work on the VLSI CAD tools
- Enclose the **photocopy of the first 5 pages of M.E. thesis** to confirm that your M.E. project is related to analog VLSI
- **The applicant should have obtained 1st class in both UG & PG**
- **The selected staff may enroll for part time PhD in ECE dept**
- Salary: Rs.15,000/= per month (consolidated)
- Duration of appointment: 18 months
- Last date & time for submission of application : 26th OCT 2015 5:00 PM
- Written test/Interview : 29th OCT 2015 2:30 PM
- Venue : Lecture Hall Complex opposite to Octagon Computer centre
- All the candidates who have submitted the application before the last date would be permitted to take the written test **if they have obtained 1st class in both UG & PG and carried out their M.E. projects in analog VLSI**
- Separate communication regarding the written test/interview would **not** be sent.

The filled application should be sent to

The Chief Investigator,

Broadcom Foundation Project,

ECE Dept., NIT, Trichy 620015

The soft copy of the application may be sent to adrc@nitt.edu with subject as

Broadcom project

Prescribed application is given in the next page

NATIONAL INSTITUTE OF TECHNOLOGY

TIRUCHIRAPPALLI- 620 015



APPLICATION FOR THE TEMPORARY POST OF RESEARCH ASSISTANT
IN BROADCOM PROJECT

Recent
Passport size
photograph to
be affixed.

1.	Name in Full (Capital Letters) (as in SSLC Certificate)				
2.	Date of Birth		Day	Month	Year
	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
3.	Address to which Communications should be sent (Also furnish email, fax, telephone number, if any)				
4.	Permanent Home Address				
5.	Name and Address of Father / Husband				
6.	Details of University Education from SSLC onwards				
	Degree	Discipline	University	Class/Division	% Marks
				on	Year of Passing

7 Professional Experience in chronological order upto the present post				
Organisation	Designation	From	To	Scale of Pay
8 List of papers published/ awards & distinctions won				
9 List of programming languages and VLSI CAD tools in which you have experience				
10 List training programmes attended				
12.	List of enclosures*.			

13. I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect/ false or if I do not satisfy the eligibility criteria, my candidature/ appointment is liable to be cancelled / terminated. I understand that the post is purely temporary in nature

Place :

Date :

Signature of the Applicant

***Enclose the photo copy of the following**

- (a) Details of publications/conference papers presented listing
- (b) Workshop/Training programmes attended
- (c) Awards, Patents, Prizes etc. indicating,
- (d) Any other relevant but brief information on your academic standing.
- (e) Copy of degree certificates , mark sheets and GATE SCORE card
- (f) photocopy of the first 5 pages of M.E. thesis to confirm that your M.E. project is related to analog VLSI

Summary sheet to be submitted in softcopy to adrc@nitt.edu

Name	Address, Mobile No	DOB	Name of UG College	% in UG	Name of PG College	% in PG