DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY, TRICHY-15

23 MAY 2012

ADVERTISEMENT

Applications are invited for the following temporary posts in the R&D project on **VLSI Implementation of Wireless Sensor networks** funded by Department of Information Technology, New Delhi:

1. Research Associate

Essential Qualifications:

I Class M.E./M.Tech in VLSI system/ Microelectronics/communication system with at least 1 year experience on VLSI CAD tools .

Consolidated Salary: Rs.20,000-22,000/- per month.

Last date for submission of the completed application: 12 JUN 2012

2. Junior Research Fellow

Essential Qualifications:

I Class B.E./B.Tech in any circuit branch.

Desirable: Exposure to HDL/ VLSI CAD tools

Qualification in GATE with valid Score

Consolidated Salary: Rs.9,000/- per month.

Last date for submission of the completed application: 12 JUN 2012

More details and application format are given in the next page

HOD/ECE

Advertisement for the post of Research associate/Junior research fellow for R&D project

on VLSI Implementation of Wireless Sensor networks funded by Department of

<u>Information Technology</u>, New Delhi:

Information to the candidates

No. of posts: 1. Research associate : One

2. Junior research fellow : One

Tenure:

The post is purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended upto a maximum of additional 1 or 2 years depending upon the performance. The tenure will not be extended beyond 3 years.

Qualification and Salary for Research associate:

Essential Qualifications:

I Class M.E./M.Tech in VLSI system/ Microelectronics/communication system with at least 1 year experience on VLSI CAD tools .

The initial salary may be fixed from Rs.20, 000 to Rs. 22,000 (consolidated) depending upon the previous experience.

Qualification and Salary for Junior Research Fellow

Essential Qualifications:

I Class B.E./B.Tech in any circuit branch

Desirable: Exposure to HDL/ VLSI CAD tools,

Qualification in GATE with valid Score .

Consolidated Salary: Rs.9,000/- per month.

Registration for Part time degrees.:

Research associate, if found suitable, can register for part time Ph.D.

Junior research fellow, if found suitable, can register for part time M.S.

Last date for submission of the completed application: 12 JUN 2012

The application should be sent to

The Coordinator, DIT project,

Department of ECE,

NIT, Tiruchirappalli – 620 015.

Superscribe the cover as "Application for the post of temporary staff in DIT project"



NATIONAL INSTITUTE OF TECHNOLOGY TIRUCHIRAPPALLI- 620 015

APPLICATION FOR THE TEMPORARY POST OF RESEARCH ASSOCIATE/JUNIOR RESEARCH FELLOW IN DIT PROJECT.

[Please tick the relevant post(s)]

Recent Passport size photograph to be affixed.

1.	Name in (as in S	n Full (Capital Let SLC Certificate)	ters)								
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4.	Perman	ent Home Addres	ss								
5. Name and Address of Father / Husband											
6. De	etails of U	Iniversity Educati	on from SSL	C onwards							
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7 Professional Experience in chronological order upto the present post						
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8	List of papers pub	lished/ awards & distinctions	s won			
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9	List of programmi	ng languages and VLSI CA	D tools is whi	oh vou hovo	ovnorionoo	
9	List of programmi	ng languages and vLSi CA	D tools in whi	ch you have	experience	
10	List training progra	ammes attended				
10	List training progre	diffines attended				
11	List of subjects					
' '	taught (if any)					
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13.	List of enclosures*.					
14.	I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect/ false or if I do not satisfy the eligibility criteria, my candidature/ appointment is liable to be cancelled / terminated. I understand that the post is purely temporary in nature					
	Place : Sign	ature of the Applicant				
		2.12.12 C. 11.10 / 1pp//00/11				

*Enclose the photo copy of the following

- (a) Details of publications/conference papers presented listing
 (b) Workshop/Training programmes attended
 (c) Awards, Patents, Prizes etc. indicating,
 (d) Any other relevant but brief information on your academic standing.
 (e) Copy of degree/diploma certificates and mark sheets