



NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI – 620 015
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

December 11, 2013

The shortlisted candidates are directed to appear for the written test for the Ph.D. programme in the Department of Electrical & Electronics Engineering as per the details given below:

Venue : Lecture Hall Complex, NIT-Tiruchirappalli
Date & Time : 20-12-2013, 10.30 a.m. – 11.30 a.m.
Written Test : Objective questions

(There is negative mark for each wrong answer. For each wrong answer, 1/4 of the marks of that question will be deducted)

You are requested to report 15 minutes before the commencement of the test at the venue. You will not be permitted to write the test without the admit card. No TA/DA will be provided for the written test. Short listed candidates from the Written Test will be interviewed at 2.00 p.m. on the same day by the expert committee.

The following are the shortlisted candidate for the Ph.D. programme (Part-Time-external/on campus) in the Department of Electrical & Electronics Engineering, NIT, Tiruchirappalli for January 2014 session:

Sl.No.	Application No.	Name of the Candidate
1.	Ph.D/EE/01	Praveen Raj, R. S.
2.	Ph.D/EE/02	Menaga, D.
3.	Ph.D/EE/03	Mahesh, M.
4.	Ph.D/EE/05	Rohit Rajan Eapen, R.
5.	Ph.D/EE/07	Lakshmanan, N.
6.	Ph.D/EE/08	Gilabert Chandra, D.
7.	Ph.D/EE/09	Meenalochani, M.
8.	Ph.D/EE/10	Sarojini Mary, S.
9.	Ph.D/EE/12	Saravanan, R.
10.	Ph.D/EE/13	Indira, D.
11.	Ph.D/EE/14	Rabeek Raja, K.
12.	Ph.D/EE/15	Gayathri Devi, B.
13.	Ph.D/EE/16	Saranya, T.
14.	Ph.D/EE/18	Suresh, C.
15.	Ph.D/EE/19	Bhuvaneswari, S.
16.	Ph.D/EE/20	Lalitha Tripura Sundari, A.O.

Head, Department of EEE