



## National Institute of Technology, Tiruchirappalli: Performa for CV of Faculty/ Staff Members

SSLC	State Board	1992	89.6%	
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### 9. Academic/Administrative Responsibilities within the University

Position	Faculty/Department/Centre/Institution	From	To
Member	Institute level NBA committee	2014 onwards	
Lab In charge	Microprocessor and Microcontroller lab	2006 onwards	
Class coordinator	PG VLSI System	2007	2008
Class coordinator	UG IV-Semester	2009	2010
Member	M.Tech Admission work, Syllabus revision work, Staff selection application processing work	2008	2010
Committee member	Department level convocation work	2011 onwards	
Project Coordinator	Phase -1 PG VLSI System	2011 onwards	
Member	Department Performance Evaluation Committee	2012	2013
Core team member	UG- NBA SAR preparation	2012	2014
Treasurer	ECE Association	2012 onwards	
Core team member	PG – VLSI SYSTEM NBA SAR preparation	2014 onwards	

### 10. Academic/Administrative Responsibilities outside the University

Position	Institution	From	To
One of the project coordinator	INTEL Internship program	2014	

### 11. Awards, Associateships etc.

Year of Award	Name of the Award	Awarding Organization
2011	Won the 3rd position in the Young Scientist category	International conference on Signal Processing, Communication, Computing and Networking Technologies (ICSCCN)
2014	Won the Best paper award	International conference on Control, Instrumentation, Communication and Computational Technologies (ICCICCT)

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### 12. Fellowships

Year of Award	Name of the Fellowship	Awarding Organization	From (Month/Year)	To (Month/Year)
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### 13. Details of Academic Work

#### (i) Curriculum Development

1. A new course Computer Architecture and Organization is introduced.
2. New topics are introduced in PG VLSI System Testing course.
3. Established the extension of Microprocessor and Microcontroller laboratory in SJB Building (2nd floor).
4. Involved in the purchase of software, Microcontroller kits and accessories for the Microprocessor and Microcontroller laboratory.
5. Purchased the ARM processors development board and few sensors for encouraging the students to do the projects in the embedded area.

#### (ii) Courses taught at Postgraduate and Undergraduate levels

##### Undergraduate levels:

1. Principles of Communication Engineering.
2. Applied Electronics
3. Advanced Microprocessors
4. Digital Electronics
5. Microprocessors and Microcontrollers
6. Computer Architecture and Organization
7. Analog Integrated & Circuits Lab
8. Devices and Networks Lab
9. Microprocessors and Microcontrollers lab

##### Postgraduate levels:

1. Basics of VLSI
2. Designing with ASICs
3. Low power VLSI
4. High speed Communication Networks
5. VLSI System Testing

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(iii) Projects guided at Postgraduate level

Year	Number of students Guided
2006-2016	M.Tech – 28    M.Sc - 3

(iv) Other contribution(s)

Guided Undergraduate level projects.

### 14. Details of Major R&D Projects

Title of Project	Funding Agency	Duration		Status
		From	To	Ongoing/ Completed
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### 15. Number of PhDs guided

Name of the PhD Scholar	Title of PhD Thesis	Role(Supervisor/ Co-Supervisor)	Year of Award
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### 16. Participation in Workshops/ Symposia/ Conferences/ Colloquia /Seminars/ Schools etc. (mentioning the role)

Date (s)	Title of Activity	Level of Event (International/ National / Local)	Role (Participant/ Speaker/ Chairperson, Paper presenter, Any other)	Event Organized by	Venue
<b>Two days</b> 23-12-2005 to 24-12-2005	Workshop on “Designing Systems on Programmable Chip (SOPC)”	Local	Participant	Dept. of ECE, NITT	NITT
<b>One day</b> 04-01-2007	The pre-conference tutorial on “Embedded Systems on Chip”	Local	Participant	Dept. of ICE, NITT	NITT
<b>One week</b> 13-03-2007 to 17-03-2007	The short term course on “Recent Trends in Processor Architecture”	Local	Participant	Dept. of CSE, NITT under QIP	NITT
<b>Two days</b>	workshop on “Electronic Circuit	Local	Participant	Dept. of EEE,	NITT

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28-09-2007 to 29-09-2007	Design Techniques”			NITT under TEQIP	
<b>One day</b> 02-11-2007	workshop on “ WiMAX, 3G- Applications in Wireless Technologies “	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
<b>Two days</b> 18-12-2007 to 19-12-2007	workshop on “ Special Topics in Microwaves”	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
<b>Two days</b> 31-03-2008 to 01-04-2008	A series of lectures on “ CMOS- MEMS device Technology and the future”	Local	Participant	Dept. of ICE, NITT	NITT
<b>One day</b> 17-04-2008	workshop on “ Biologically Inspired Algorithms for Digital Signal Processing Applications”	Local	Participant	Dept. of ECE, NITT under TEQIP	NITT
<b>Two days</b> 14-05-2008 to 15-05-2008	workshop on “ Internet Technologies and Tools”	Local	Participant	Dept. of CSE, NITT under TEQIP	NITT
<b>Two weeks</b> 16-06-2008 to 28-06-2008	staff development programme on “Fuzzy Logic : Theory and Engineering Practices”	Local	Participant	Dept. of Mathematics & Production Engineering, NITT MHRD – AICTE sponsored	NITT
<b>One week</b> 21.08.2008 to 27.08.2008	Induction Training programme on “Instructional Design And Delivery System”	Local	Participant	NITTTR , Chennai MHRD sponsored	NITT
<b>Two days</b> 23.01.2009 to 24.01.2009	workshop on “Design of High Performance Systems Using FPGAs “	Local	Participant	Dept. of ECE, NITT	NITT
<b>Two weeks</b> 15-06-2009 to 27-06-2009	staff development programme on “Engineering Practices on Fuzzy Logic, Neural Networks and Hybrid Intelligent Systems”	Local	Participant	Dept. of Mathematics & Electrical & Electronics Engineering, NITT MHRD – AICTE sponsored	NITT
<b>One week</b> 28-06-2010 to 03-07-2010	staff development programme on “Recent Trends in Biomedical Systems”	Local	Participant	Dept. of ICE, NITT	NITT
<b>One week</b> 13-07-2010 to 17-07-2010	staff development programme on “Recent Trends in Microwave Integrated Circuits”	Local	Participant	Dept. of ECE, NITT	NITT
<b>Two days</b> 05-10-2012 to 06-10-2012	Faculty Oriented Programme on “Class Room Management & Communication”	Local	Participant	Dept.of Humanities	Hotel Sangam, Tanjore.

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<b>Two weeks</b> 04-06.2013 to 14-06-2013	ISTE workshop on “Analog Electronics”	National	Participant	IIT Kharagpur National mission on Education through ICT (MHRD)	NITT
<b>Two days</b> 26-08.2013 to 27-08-2013	workshop on “Recent Trends in Wireless Networks”	Local	Participant	Dept. of ECE, NITT, TEQIP-II sponsored	NITT
<b>Two weeks</b> 02-01.2014 to 12-01-2014	ISTE Workshop on “Signals and Systems”	National	Participant	IIT Kharagpur National mission on Education through ICT (MHRD)	NITT
<b>Two days</b> 12-12.2014 to 13-12-2014	Workshop on “Big Data Analytics”	Local	Participant	Dept. of CA and Dept. of CSE, NITT	NITT
<b>Three days</b> 15-12.2014 to 17-12-2014	workshop on “Real Time Signal Acquisition & Processing Using Open Source Software SCILAB”	Local	Participant	Dept. of ICE, NITT	NITT
<b>Two days</b> 28-04.2015 to 29-04-2015	“Conclave on Academic Reforms” (CAR 2015)	Local	Participant	NITT	NITT
<b>Two days</b> 13-11.2015 to 14-11-2015	workshop on “Internet of Things & Smart Home”	Local	Participant	Dept. of EEE, NITT	NITT

17. Workshops/ Symposia/ Conferences/ Colloquia/Seminars Organized (as Chairman/ Organizing Secretary/ Convenor / Co-Convenor)

Title of Activity	Level of Event (International/ National/ Local)	Date (s)	Role	Venue
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18. Invited Talks delivered

Topic	Date	Inviting Organization
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19. Membership of Learned Societies

Type of Membership (Ordinary Member/ Honorary Member / Life Member )	Organization	Membership No. with date

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Member	IEEE	93852139
Life Member	ISTE	LM31319

20. Academic Foreign Visits

Country	Duration of Visit	Programme
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21. Publications

(A) Refereed Research Journals:

Author(s)	Title of Paper	Journal	Volume (No.)	Page numbers	Year	Impact Factor of the Journal (Optional)
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(B) Conferences/Workshops/Symposia Proceedings

Author(s)	Title of Abstract/ Paper	Title of the Proceedings	Page numbers	Conference Theme	Venue	Year
P.Palanisamy, R.Thilagavathy, M.Ratheesh Kumar and A.Srihari	Efficient realization of CORDIC based LDPC Decoder for WiMAX System	IEEE	Pp 46- 50.	International Conference on Signal Processing, Communications and Networking	Ann University Chennai	2008
Kirankumar, M.Bhaskar, R.Thilagavathy and Dr.B.Venkataramani	Novel Low Power Multilevel Current Mode Interconnect System Ineffective to Supply voltage variations			International Conference on Optoelectronics and Communication Technology	Trivandrum	2009
R.Raghavendra, R.Thilagavathy	Asynchronous Reconfigurable CRC Circuit in Sense Amplifier – Based Pass Transistor Logic	IEEE		2nd National conference on Signal processing, Communications and VLSI Design	Anna University Coimbatore	2010

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Zameeruddin P, R.Thilagavathy	High Performance Low– Power Carry Look- ahead Adder	IEEE		2nd National conference on Signal processing, Communications and VLSI Design	Anna University Coimbatore	2010
G Vidhya Sagar, R.Thilagavathy	A Multi-Phase Delay Locked Loop with Bist Circuit for Fault Detection	IEEE		2nd National conference on Signal processing, Communications and VLSI Design	Anna University Coimbatore	2010
G.Gopikiran, R.Thilagavathy	FPGA Implementation of Floating point Rotation Mode CORDIC Algorithm	IEEE		International Conference on Signal Processing , Communication, Computing and Networking Technologies	Noorul Islam University, Kumaracoil	2011
V.Mohana Vidya, R.Thilagavathy	Low Power, High Performance Current Mode Transceiver for Network-on- Chip Communication	IEEE		International Conference on Signal Processing, Communication, Computing and Networking Technologies	Noorul Islam University, Kumaracoil	2011
G.Gopikiran, R.Thilagavathy, P.Pavan Kumar	FPGA Implementation of Floating - point CORDIC Arithmetic for SVD Computation	Sponsere d by Elsevier		International Conference on Advanced Computing Methodologies	Gokaraju Rangaraju Institute of Engineering and Technology ,Hyderabad	2011
T.Manoj Sharma, R.Thilagavathy	Performance Analysis of Advanced Encryption Standard for Low Power and Area Applications	IEEE		International Conference on Information and Communication Technologies	Noorul Islam University, Kumaracoil	2013
K. G. Digish, R. Thilagavathy	ASIC Implementation of Physical Downlink Shared Channel for LTE	IEEE		International Conference on Control, Instrumentation, Communication and Computational Technologies	Noorul Islam University, Kumaracoil	2014
Sandeep K, R	Performance Comparison	IEEE		International	Noorul	2014



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Thilagavathy	of Physical Downlink Control Channel (PDCCH) for LTE employing QPSK and 16QAM as Modulation Schemes			Conference on Control, Instrumentation, Communication and Computational Technologies	Islam University, Kumaracoil	
R. Rahul, R. Thilagavathy	A Low Phase Noise CMOS Voltage-Controlled Differential Ring Oscillator	IEEE		International Conference on Control, Instrumentation, Communication and Computational Technologies	Noorul Islam University, Kumaracoil	2014
K Srikanth Babu, R. Thilagavathy	Rapid Estimation of Circuit Performance	organized by International Association of Science, Technology and Management		International Conference on Computing, Communication and Sensor Network	PIET, Odisha	2014

**(C) Books & Monographs**

Author(s)	Title of Book/Monograph	Name of Publishers	Year of Publication	ISSN/ISBN Number
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I, R.Thilagavathy, Assistant Professor in the Department of Electronics and Communication Engineering, NIT, Trichy. I have completed B.E degree in Electronics and Communication Engineering in Government college of Engineering, Salem and finished M.E degree in VLSI System in REC, Trichy. Currently, I am pursuing Ph.D in Big Data Analysis in Bio medical Applications under the guidance of Dr. B.Venkataramani, Professor, ECE, NIT, Trichy.

My areas of interest are Microprocessors, Microcontrollers and Low power VLSI system design. I am guiding real time Embedded systems and IOT related projects for all branches of undergraduate students. I have published 10 International and 3 National conferences papers. I have participated in many workshops related to IOT.