

TENTATIVE SCHEDULE

SEMESTER EXAMINATIONS, NOVEMBER – 2016

B.Tech. V, & VII Semester Theory Examinations

Date	9:30 a.m. to 12:30 p.m.	2:00 p.m. to 5:00 p.m.
15-11-2016	V (B.Tech.) SLOT A	VII (B.Tech.) SLOT A
16-11-2016	V (B.Tech.) SLOT B	VII (B.Tech.) SLOT B
17-11-2016	V (B.Tech.) SLOT C	VII (B.Tech.) SLOT C
18-11-2016	V (B.Tech.) SLOT D	VII (B.Tech.) SLOT D
19-11-2016	----	VII (B.Tech.) SLOT I
21-11-2016	V (B.Tech.) SLOT E	VII (B.Tech.) SLOT E
22-11-2016	V (B.Tech.) SLOT F	VII (B.Tech.) SLOT F
23-11-2016	V (B.Tech.) SLOT G	VII (B.Tech.) SLOT F1
24-11-2016	V (B.Tech.) SLOT H	VII (B.Tech.) SLOT G
25-11-2016	V (B.Tech.) SLOT I (HONOURS)	VII (B.Tech.) SLOT H

* HODs are requested to scheduled the written test for the Comprehensive Examinations on or before 14/11/2016 (1 hour) in their respective departments. The planned schedule shall be sent to the under signed.

Date: 13-10-2016

Deputy Registrar (Academic)

cc: All Heads of the Departments

All Notice Boards of Institute & Hostels / First Year Coordinator

Professor of Training & Placement/ Librarian/ Hostel Manager/Physical Director/

Copy Submitted to the Director.

Assistant Engineer, Electrical/EMD with a request to provide power supply during power cut times.