



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY
TIRUCHIRAPPALLI-620 015

Date: 14.02.2019

Notice of Ph.D. Viva-voce

Mr. A. Dheepanchakkravarthy (Roll No: 407912051), part time (on campus) research scholar, Department of Electrical and Electronics Engineering, will defend his Ph.D. thesis in a public viva-voce as per the following schedule:

Date : 01-03-2019
Time : 09.30 AM
Venue : Seminar Hall, EEE Department
Thesis Title : FPGA Controlled Four-leg DSTATCOM for Multifarious Load Compensation in Power Distribution System

A copy of the thesis is available in the department for reference. All are cordially invited to attend the viva-voce and take part in the discussion.


Dr. S. Moorthi
Research Guide


Dr. M.P. Selvan
Research Co-Guide


Chairman/DC


Head/EEE

Copy to:

The Director, NITT for kind information
The Examiners & Doctoral Committee Members
All Deans
All HoDs with a request to kindly arrange to display in the Dept. notice board
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